

Engineering/Process Change Notice

ECN/PCN No.: 4433

For Manufacturer								
Product Description: CMOS Output Programmable		Abracon Part Number / Part Series: AP2S			☐ Documentation o ☑ ECN ☐ EOL	only	☑ Series☐ Part Number	
Affected Revision:	N	New Revision	n:	P		Application:		☐ Safety ☑ Non-Safety
Prior to Change:								, <u> </u>
Input Current:								
$V_{dd} = 3.3V$ $V_{dd} = 2.5V$ $V_{dd} = 1.8V$ $V_{dd} =$		35 30 mA						
Rise & Fall Time:	1 1	20	ı					
V _{dd} = 3.3V		2						
Rise and Fall Time $V_{dd} = 2.5V$		3 4	ıs					
VOH/VOL:								
Output Voltage Vol		10% of	Vdd	Vde				
V _{OH}	90% of Vdd			ſ				
Stanby Current:				1 400		77 10050077	1	
Standby current (Power Down op	tion)			400	uA	$V_{dd} = 1.8, 2.5, 3.3V$		
RMS Phase Jitter: RMS Phase Jitter @25°C± 3°C	1	2	ps	Vdd=3.3V				
(10 - 39MHz: 12kHz to 5MHz)	1.1	2	ps	Vdd=2.5V				
(>39MHz: 12kHz to 20MHz)	1.5	2	ps	Vdd=1.8V				
Notes:								
* Inclusive of calibration @25°C, operatin vibration. For ±20ppm Overall Frequency shock, and vibration. ** Transition times are measured between	/ Stability: Inclusive o	f calibration @25	°C, oper	ating temperature rang	ging, shock e, load vari	c, and ation,		
Height:	1070 and 2070 01 vad	with all output it	AG 01 15	pr.				
Tielgite.	= +5							
0.29	0.81±0.15							
Land Pattern:	0							
Recommended land pattern								
	2X 0.035 2X 0.9)							
0.03								
Operating Temperature:								
Operating Temp. I: 0°C ~ +50°C E: -20°C ~ +70°C								
F: -30°C ~ +70°C								
N: -30°C ~ +85°C								
L: -40°C ~ +85°C								
After Change:								
Input Current:								
$V_{dd} = 3.3V$ $V_{dd} = 2.5V$		40 35						
Input Current		33	mA					





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Rise & Fall Time:

$V_{dd} = 3.3V$		4	
$V_{dd} = 2.5V$		5	
$V_{dd} = 1.8V$		6	ns

(Tr/Tf)*** VOH/VOI ·

Rise and Fall Time

VOI I/ VOL.				
Output Voltage	Vol		0.4	V
	VoH	Vdd - 0.4		

Stanby Current:

Standby current (Power Down option)	<400	uA	$V_{dd} = 1.8, 2.5, 3.3V$
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RMS Phase Jitter:

RMS Phase Jitter***	1	2	ps	Vdd=3.3V
@25°C± 3°C	1.1	2	ps	Vdd=2.5V
(10 - 39MHz: 12kHz to 5MHz)	1.5	2.2	ps	Vdd=1.8V
(>39MHz: 12kHz to 20MHz)			_	

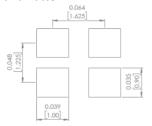
Notes:

- * Inclusive of calibration tolerance @25°C, operating temperature range, input voltage variation, load variation, and first year aging. For ±20ppm Overall Frequency Stability: Inclusive of calibration tolerance @25°C, operating temperature range, and load variation.
- ** Only 40/60% is available for certain frequencies. Please contact Abracon when ordering.
- *** Transition times are measured between 10% and 90% of Vdd with an output load of 15 pF.
- **** Frequency dependent, contact factory.

Height:



Land Pattern:



Operating Temperature:

Operating Temp.

Blank: -10°C \sim +60°C
I: 0° C $\sim +50^{\circ}$ C
E: -20°C ~ +70°C
F: -30°C ~ +70°C
N: -30°C ~ +85°C
L: -40°C ~ +85°C

Cause/Reason for Change:

Updated electrical spcifications due to introduction of next generation IC.

Change Plan						
Effective Date: 9/14/2022	Additional Remarks:					
Change Declaration:						
Issued Date:	Issued By:	Issued Department:				
9/14/2022	Arturo Longoria	Engineering				
Approval:	Approval:	Approval:				
Thomas Culhane	Reuben Quintanilla	Ying Huang				
Engineering Director	Quality Director	Purchasing Director				

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Form #7020 | Rev. G | Effective: 02/22/2021 |

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	For Abrac	on EOL only		
Last Time Buy (if applicable):	4	Alternate Part Number / Part Series: N/A		
Additional Approval:	Additional Approval:		Additional Approval:	
	Customer Appro	val (If Applicab	le)	
Qualification Status:				
	☐ Approved [☐ Not accepted		
Note: It is considered approved if	f there is no feedback from the cเ	ustomer 1 month	after ECN/PCN is released.	
Customer Part Number:		Customer Proje	ect:	
Company Name:	Company Representa	ative:	Representative Signature:	
Customer Remarks:			'	













