

6-CHANNEL ELECTRONIC VOLUME

■ GENERAL DESCRIPTION

The **NJW1150** is a 6-CHANNEL ELECTRONIC VOLUME, which also includes tone control, balance and trim level control. The **NJW1150** is suitable for multi-channel audio system, such as AV amplifier, mini stereo component, speaker system, and others.

All of internal status and variables are controlled by I²C BUS interface.

■ PACKAGE OUTLINE



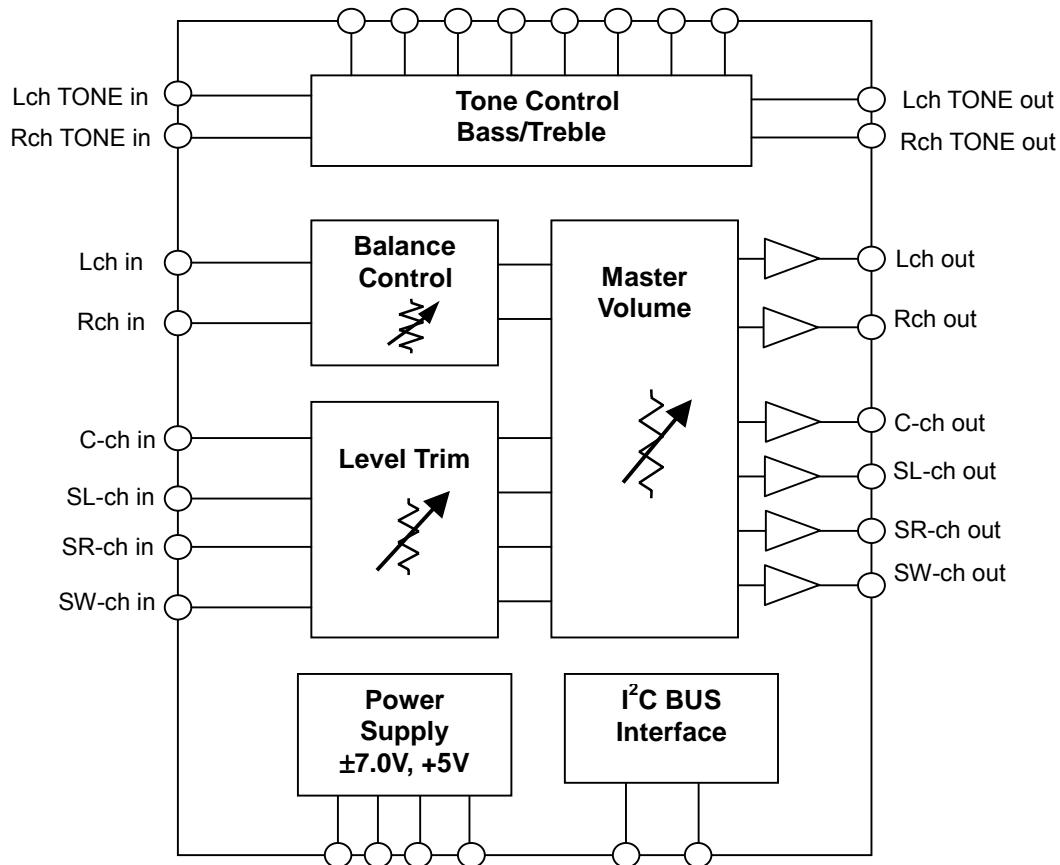
NJW1150M

■ FEATURES

- Operating Voltage

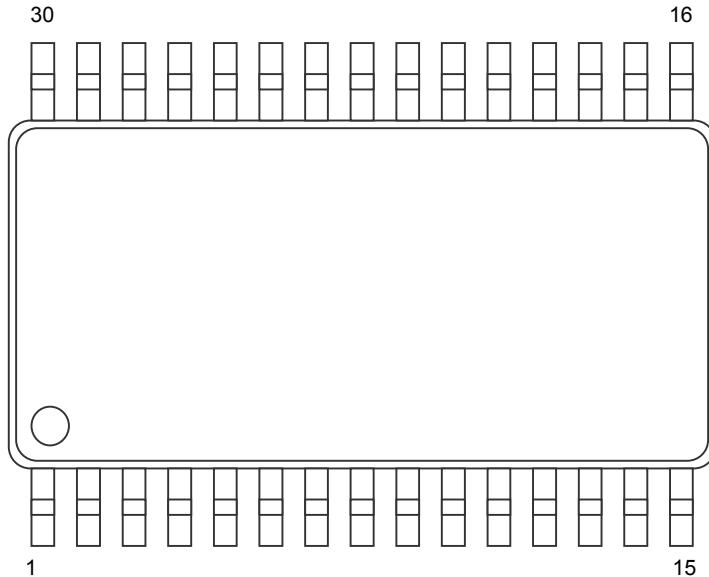
Analog Supply	±4.5 to ±7.5V
Digital Supply	+4.5 to +5.5V
- I²C BUS Interface
- 6-Chnnel Master Volume
- Balance control for L, R-ch
- Trim Level Control for C, SL, SR, SW-ch
- Independent Tone Control (Bass, Treble) for L, R-ch
- Bi-CMOS Technology
- Package Outline SDMP30

■ BLOCK DIAGRAM



NJW1150

■ PIN FUNCTION



No.	SYMBOL	FUNCTION	No.	SYMBOL	FUNCTION
1	GND	Ground	16	SCL	I ² C bus clock input
2	V+	Positive power supply voltage	17	SDA	I ² C bus data input
3	VDD	Power supply voltage (Digital)	18	SWOUT	Volume Sub Woofer channel output
4	RTIN	Tone control Right channel input	19	SROUT	Volume Surround Right channel output
5	RTOUT	Tone control Right channel output	20	SLOUT	Volume Surround Left channel volume output
6	RTC	Tone control Right channel Treble filter capacitor	21	COUT	Volume Center channel output
7	RBC1	Tone control Right channel Bass filter capacitor	22	ROUT	Volume Right channel output
8	RBC2	Tone control Right channel Bass filter capacitor	23	LOUT	Volume Left channel output
9	RBC3	Tone control Right channel Bass DC cut capacitor	24	LBC3	Tone control Left channel Bass DC cut capacitor
10	LIN	Volume Left channel input	25	LBC2	Tone control Left channel Bass filter capacitor
11	RIN	Volume Right channel input	26	LBC1	Tone control Left channel Bass filter capacitor
12	CIN	Volume Center channel input	27	LTC	Tone control Left channel Treble filter capacitor
13	SLIN	Volume Surround Left channel input	28	LTOUT	Tone control Left channel output
14	SRIN	Volume Surround Right channel input	29	LTIN	Tone control Left channel input
15	SWIN	Volume Sub Woofer channel input	30	V-	Negative power supply voltage (IC substrate)

■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage 1	V ⁺ /V ⁻	+7.5/-7.5	V
Supply Voltage 2	V _{DD}	7.0	V
Maximum Input Voltage	V _{IM}	V ⁺ /V ⁻ (*)	V
Power Dissipation	P _D	700	mW
Operating Temperature Range	T _{opr}	-20 to +85	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

(*) For the maximum input voltage less than V⁺/V⁻■ ELECTRICAL CHARACTERISTICS (Ta=25°C, V⁺/V⁻=±7V, V_{DD}=5V)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply						
Operating Voltage 1	V ⁺ /V ⁻		4.5	7.0	7.5	V
Operating Voltage 2	V _{DD}		4.5	5.0	5.5	V
Supply Current 1	I _{CC}	No Signal	-	8	14	mA
Supply Current 2	I _{EE}	No Signal	-	8	14	mA
Supply Current 3	I _{DD}	No Signal	-	10	100	μA

Input/Output Characteristics

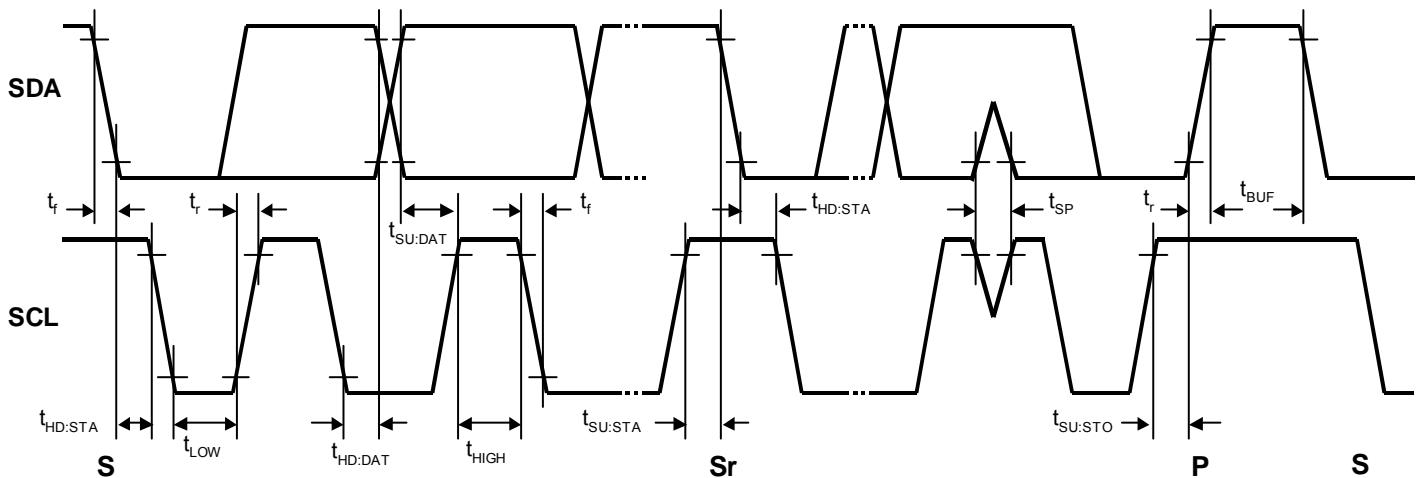
Voltage Gain	G _V	V _{IN} =1Vrms, f=1kHz Master=0dB, Balance=0dB Trimmer=0dB	-0.5	0	0.5	dB
Voltage Gain Error	ΔG _V	V _{IN} =1Vrms, f=1kHz Master=0dB	-0.5	0	0.5	dB
Maximum Attenuation 1	A _{TT1}	f=1KHz, V _{IN} =1Vrms Master=-79dB	-	-79	-	dB
Maximum Attenuation 2	A _{TT2}	f=1KHz, V _{IN} =1Vrms Mute	-	-90	-	dB
Attenuation Error	ΔA _{TT}	f=1KHz, V _{IN} =1Vrms Master=-50dB Trimmer=-10dB	-1	0	1	dB
Maximum Output Voltage	V _{OM}	f=1KHz, THD=1% Master=0dB	3.0	4.0	-	Vrms
Output Noise	V _{NO}	Master=0dB, Rg=0,A-weight	-	-110 (3.2μ)	-100 (10μ)	dBV (Vrms)
Total Harmonic Distortion	T.H.D	f=1KHz, Vo=1Vrms, Master=0dB, Trimmer=0dB	-	0.005	0.05	%
Channel Separation	CS	f=1KHz, Vo=1Vrms Master=0dB,A-weight	-	-90	-80	dB

Tone Control Characteristics

Treble Boost Gain1	H _{F_{BST}}	Vo=1Vrms f=10KHz Treble=10dB	8	10	12	dB
Treble Boost Gain2	H _{F_{CUT}}	Vo=1Vrms f=10KHz Treble=-10dB	-12	-10	-8	dB
Bass Boost Gain1	L _{F_{BST}}	Vo=1Vrms f=50Hz Bass=10dB	8	10	12	dB
Bass Boost Gain2	L _{F_{CUT}}	Vo=1Vrms f=50Hz Bass=-10dB	-12	-10	-8	dB

NJW1150

■TIMING ON THE I²C BUS (SDA,SCL)



■CHARACTERISTICS OF I/O STAGES FOR I²C BUS (SDA,SCL)

I²C BUS Load Conditions

STANDARD MODE: Pull up resistance 4kΩ (Connected to +5V), Load capacitance 200pF (Connected to GND)

PARAMETER	SYMBOL	Standard mode			UNIT
		MIN.	TYP.	MAX.	
Low Level Input Voltage	V _{IL}	0.0	-	1.5	V
High Level Input Voltage	V _{IH}	3.0	-	5.0	V
Low level output voltage (3mA at SDA pin)	V _{OL}	0	-	0.4	V
Input current each I/O pin with an input voltage between 0.1V _{DD} and 0.9V _{DDmax}	I _i	-10	-	10	µA

■CHARACTERISTICS OF BUS LINES (SDA,SCL) FOR I²C-BUS DEVICES

PARAMETER	SYMBOL	Standard mode			UNIT
		MIN.	TYP.	MAX.	
SCL clock frequency	f _{SCL}	-	-	100	kHz
Hold time (repeated) START condition.	t _{HD:STA}	4.0	-	-	μs
Low period of the SCL clock	t _{LOW}	4.7	-	-	μs
High period of the SCL clock	t _{HIGH}	4.0	-	-	μs
Set-up time for a repeated START condition	t _{SU:STA}	4.7	-	-	μs
Data hold time ^{NOTE)}	t _{HD:DAT}	0	-	-	μs
Data set-up time	t _{SU:DAT}	250	-	-	ns
Rise time of both SDA and SCL signals	t _r	-	-	1000	ns
Fall time of both SDA and SCL signals	t _f	-	-	300	ns
Set-up time for STOP condition	t _{SU:STO}	4.0	-	-	μs
Bus free time between a STOP and START condition	t _{BUF}	4.7	-	-	μs
Capacitive load for each bus line	C _b	-	-	400	pF
Noise margin at the Low level	V _{nL}	0.5	-	-	V
Noise margin at the High level	V _{nH}	1	-	-	V

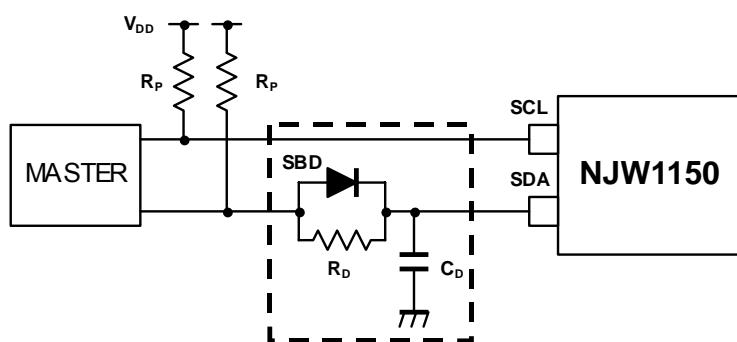
C_b ; total capacitance of one bus line in pF.NOTE). Data hold time : t_{HD:DAT}Please hold the Data Hold Time (t_{HD:DAT}) to 300ns or more to avoid status of unstable at SCL falling edge.

The SDA block in the NJW1150 does not hold data. Add external data-delay-circuit of the SDA terminal, in case of not providing a hold time of at least 300nsec for the SDA in the master device.

The time-consists of the data-delay-circuit of the SDA terminal are as follows.

- (a) Low level → High level: T_{LH} ≈ R_P*C_D
 (b) High level → Low level: T_{HL} ≈ R_D*C_D

In addition, Schottky barrier diode (SBD) influences a Low level at the Acknowledge. Therefore choose the low forward voltage (Vf) as much as possible.



NJW1150

■ TERMINAL DESCRIPTION

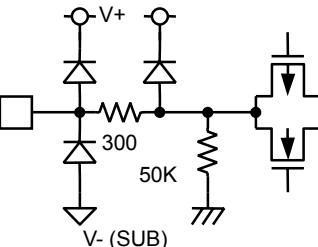
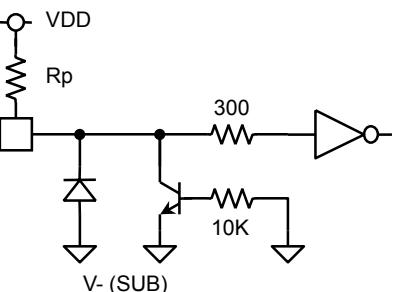
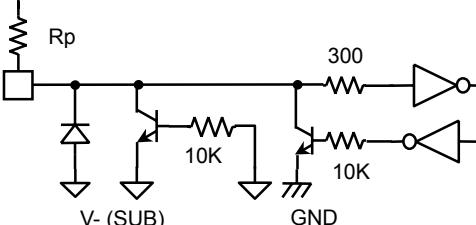
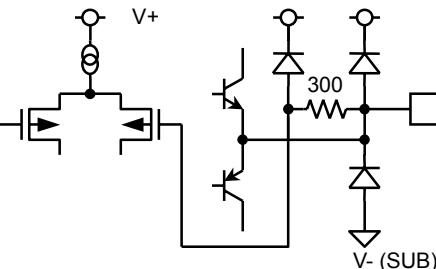
PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	TERMINAL VOLTAGE
1	GND	Ground		0
2 3	V+ VDD	Positive power supply voltage Power supply voltage (Digital)		V+ VDD
4 29	RTIN LTIN	Tone control Right channel input Left channel input		0
5 28	RTOUT LTOU	Tone control Right channel output Left channel output		0

■ TERMINAL DESCRIPTION

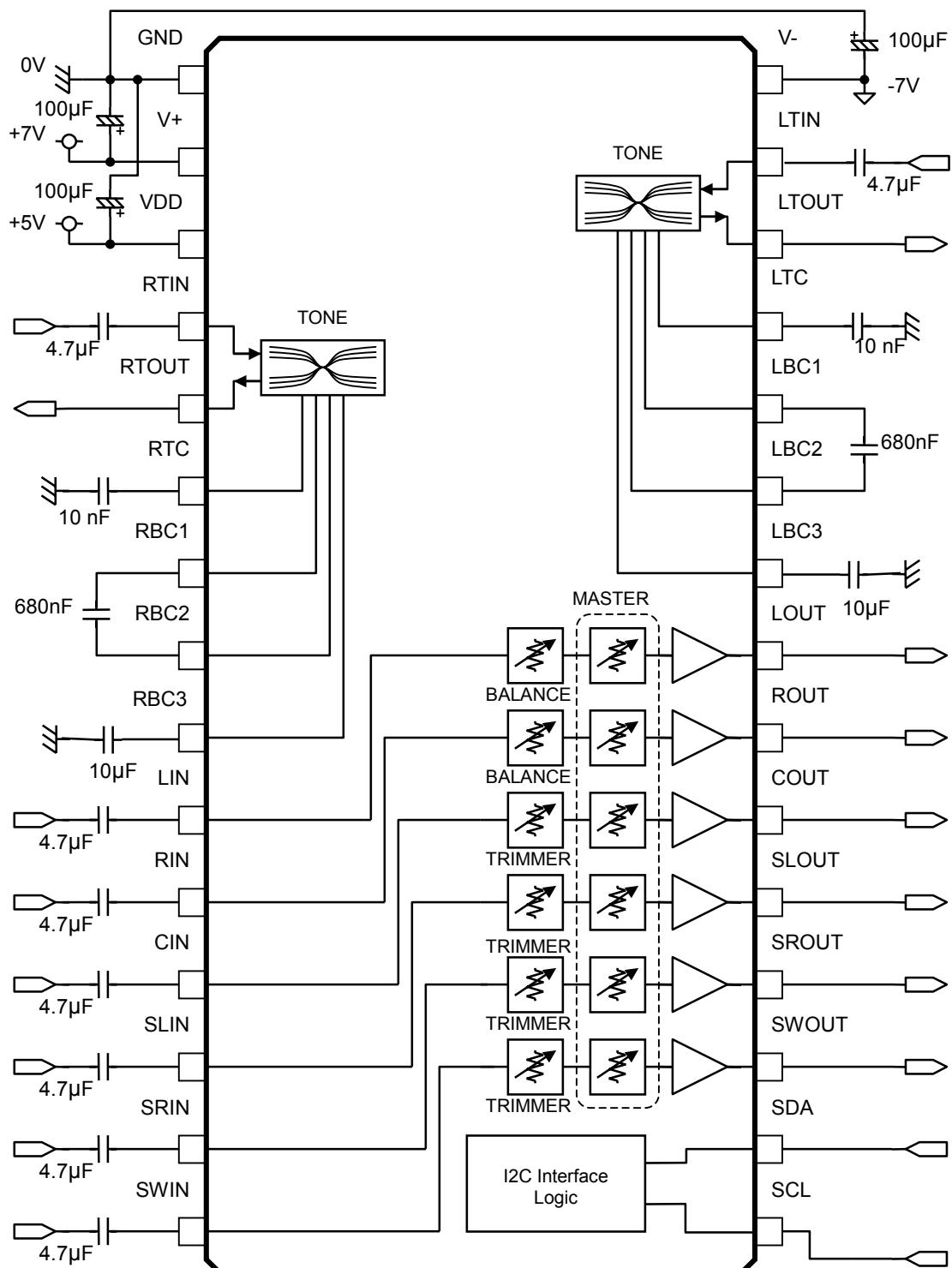
PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	TERMINAL VOLTAGE
6 27	RTC LTC	Tone control Right channel Treble filter capacitor Left channel Treble filter capacitor		0
7 26	RBC1 LBC1	Tone control Right channel Bass filter capacitor Left channel Bass filter capacitor		0
8 25	RBC2 LBC2	Tone control Right channel Bass filter capacitor Left channel Bass filter capacitor		0
9 24	RBC3 LBC3	Tone control Right channel Bass DC cut capacitor Left channel Bass DC cut capacitor		0

NJW1150

■ TERMINAL DESCRIPTION

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	TERMINAL VOLTAGE
10 11 12 13 14 15	LIN RIN CIN SLIN SRIN SWIN	Volume Left channel input Right channel input Center channel input Surround Left channel input Surround Right channel input Sub Woofer channel input		0
16	SCL	I ² C bus clock input		5
17	SDA	I ² C bus data input		5
18 19 20 21 22 23	SWOUT SROUT SLOUT COUT ROUT LOUT	Volume Left channel output Right channel output Center channel output Surround Left channel output Surround Right channel output Sub Woofer channel output		0

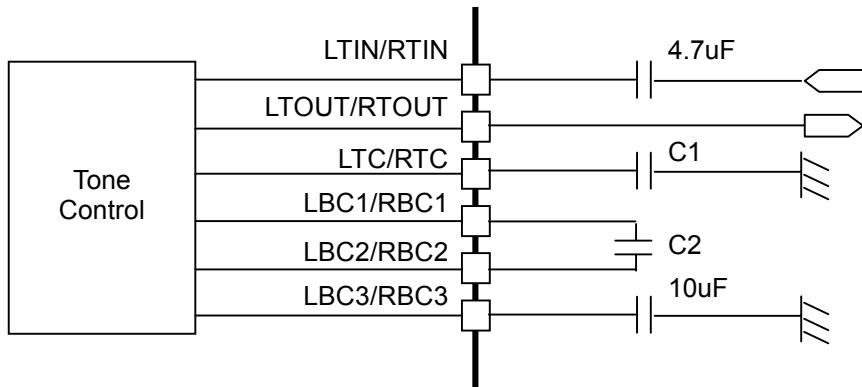
■APPLICATION CIRCUIT



NJW1150

■ Definition of cut-off frequency for Tone Control

Cut-off frequency for Tone Control is adjustable with changing the capacitor C1, C2 in below circuit.
See the next function for the cut-off frequency.



EX) C1 =10nF, C2 = 680nF
Cut-off frequency for Treble = 3.9kHz
Cut-off frequency for Bass = 238Hz

1) TREBLE =+/-10dB

2) BASS =+/-10dB

$$fc = \frac{39.2 * 10^{-6}}{C1} (\text{kHz})$$

$$fc = \frac{161.7 * 10^{-6}}{C2} (\text{Hz})$$

■ DEFINITION OF I²C REGISTER

● I²C BUS FORMAT

S: Starting Term

A: Acknowledge Bit

P: Ending Term

MSB	LSB	MSB	LSB	MSB	LSB		
S	Slave Address	A	Select Address	A	Data	A	P
1bit	8bit	1bit	8bit	1bit	8bit	1bit	1bit

● SLAVE ADDRESS

MSB	1	0	0	0	1	0	0	R/W	—TSB
-----	---	---	---	---	---	---	---	-----	------

R/W=0: Receive Only

R/W=1: No Output Data

● CONTROL REGISTER TABLE

Select Address	BIT								
	D7	D6	D5	D4	D3	D2	D1	D0	
00H	*	Master Volume							
01H	*			Left channel Balance					
02H	*			Right channel Balance					
03H	*			Center Channel Trimmer					
04H	*			SL Channel Trimmer					
05H	*			SR Channel Trimmer					
06H	*			SW Channel Trimmer					
07H	Tone (Treble)					Tone (Bass)			
08H	*	MUTE							

*: Don't care.

On Power up, the master volume mute is activated.

● CONTROL REGISTER DEFAULT VALUE

Select Address	BIT							
	D7	D6	D5	D4	D3	D2	D1	D0
00H	0	1	0	1	0	0	0	0
01H	0	0	0	0	0	0	0	0
02H	0	0	0	0	0	0	0	0
03H	0	0	0	0	1	0	1	0
04H	0	0	0	0	1	0	1	0
05H	0	0	0	0	1	0	1	0
06H	0	0	0	0	1	0	1	0
07H	0	0	0	0	0	0	0	0
08H	0	0	0	0	0	0	0	0

NJW1150

● CONTROL COMMAND TABLE

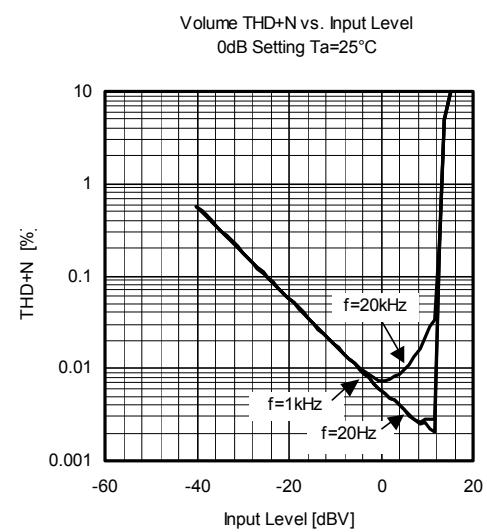
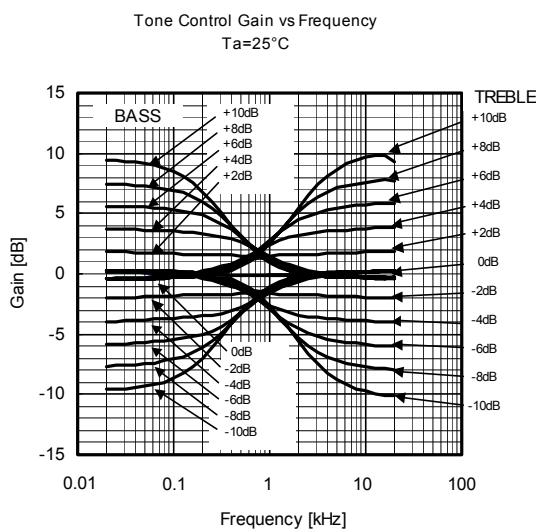
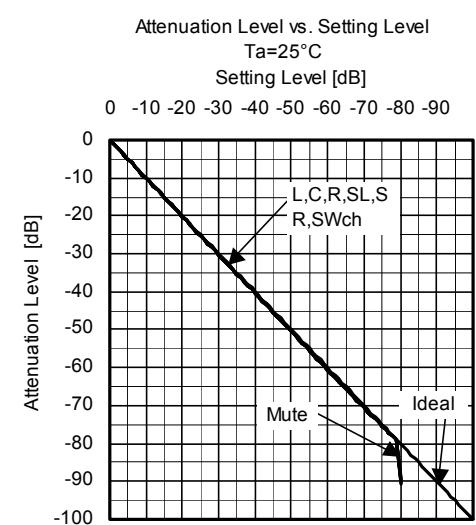
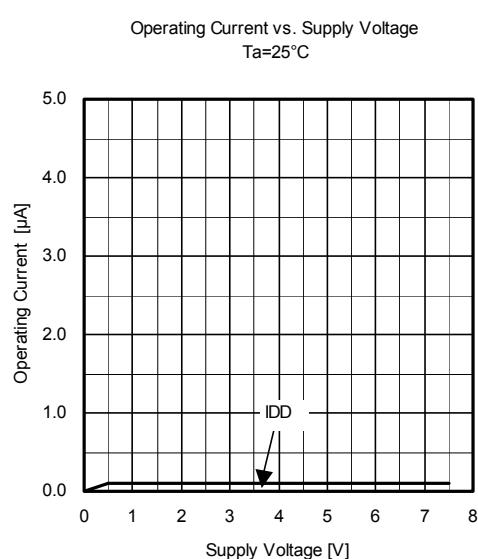
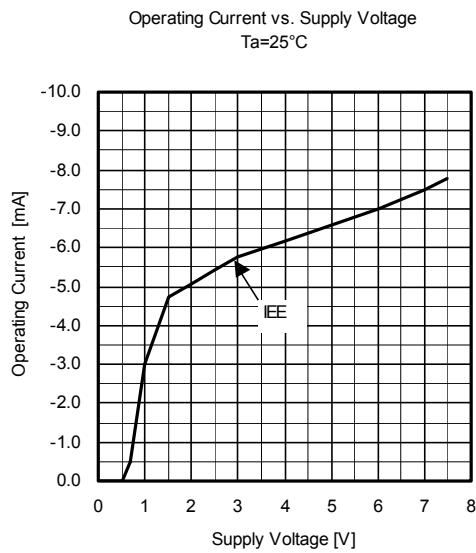
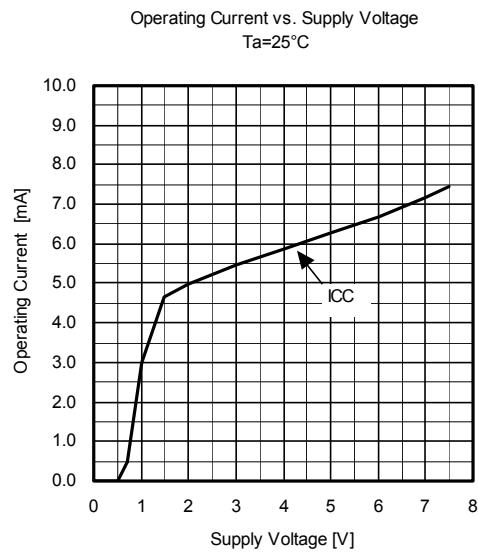
Select Address	BIT								REMARKS
	D7	D6	D5	D4	D3	D2	D1	D0	
00H	*	VOL							
		0	0	0	0	0	0	0	VOL : Volume control for all channel (1dB/step) Ex.)
		0	0	0	0	0	0	1	"000000"= 0dB "000001"=-1dB "000010"=- 2dB
		0	0	0	0	0	1	0	:
		1	0	0	1	1	1	1	"100111"=-79dB "101000"=MUTE:Default Value Maximum Attenuation : Master Volume :-79dB Trimmer -20dB
		1	0	1	0	0	0	0	Minimum Attenuation : Master Volume 0dB Trimmer 0dB
		1	1	1	1	1	1	1	
01H	*	L-BAL							
		0	0	0	0	0	0	0	L-BAL : Balance control for Left channel (1dB/step)
		0	0	0	0	0	1	1	Ex.)
		0	0	0	1	0	0	1	"0000"=0dB "00001"=-1dB
		1	1	1	1	1	1	1	:
02H	*	R-BAL							
		0	0	0	0	0	0	0	R-BAL : Balance control for Right channel (1dB/step)
		0	0	0	0	0	1	1	Ex.)
		0	0	0	1	0	0	1	"0000"=0dB "00001"=-1dB
		1	1	1	1	1	1	1	:
03H	*	C TRIM							
		0	0	0	0	0	0	0	C TRIM :Center Channel Trimmer Control
		0	0	0	0	0	1	1	Ex.)
		0	0	0	1	0	0	0	"0000"=0dB "00001"=-1dB
		1	0	1	0	0	0	0	:
04H	*	SL TRIM							
		0	0	0	0	0	0	0	SL TRIM :SL Channel Trimmer Control
		0	0	0	0	0	1	1	Ex.)
		0	0	0	1	0	0	0	"0000"=0dB "00001"=-1dB
		1	0	1	0	0	0	0	:
05H	*	SR TRIM							
		0	0	0	0	0	0	0	SR TRIM :SR Channel Trimmer Control
		0	0	0	0	0	1	1	Ex.)
		0	0	0	1	0	0	0	"0000"=0dB "00001"=-1dB
		1	0	1	0	0	0	0	:
06H	*	SW TRIM							
		0	0	0	0	0	0	0	SW TRIM :SW Channel Trimmer Control
		0	0	0	0	0	1	1	Ex.)
		0	0	0	1	0	0	0	"0000"=0dB "00001"=-1dB
		1	0	1	0	0	0	0	:
									"10100"=-20dB "00001010"=-10dB Default Value

● CONTROL COMMAND TABLE

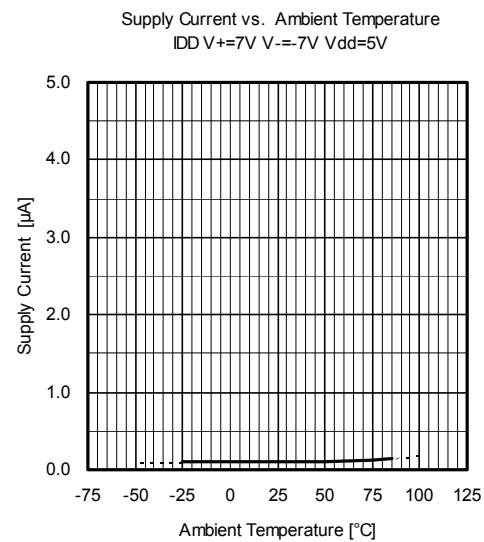
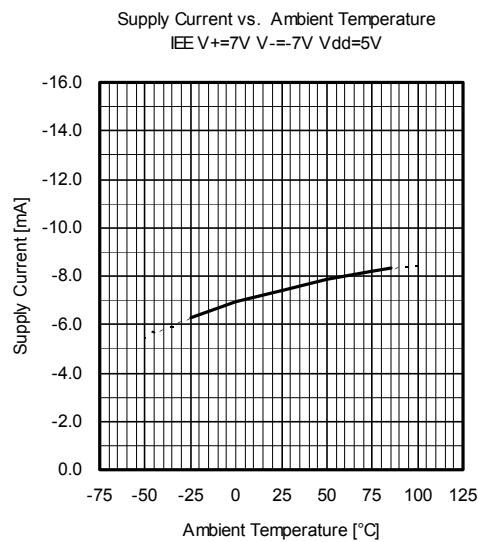
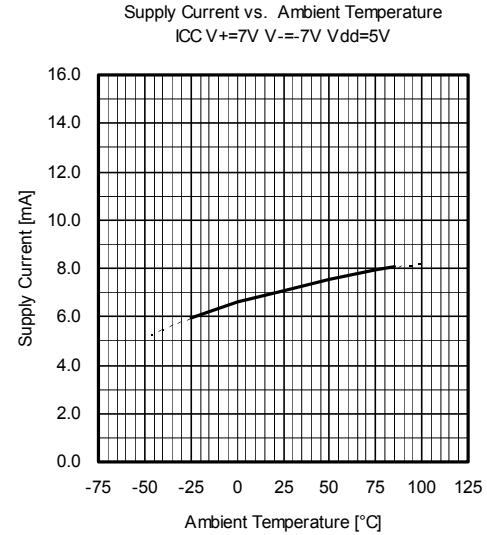
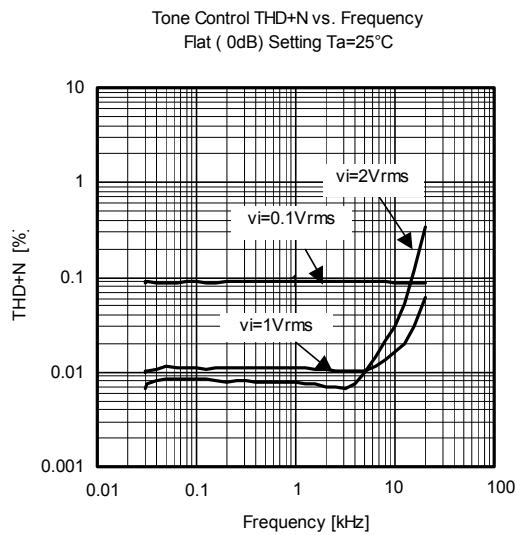
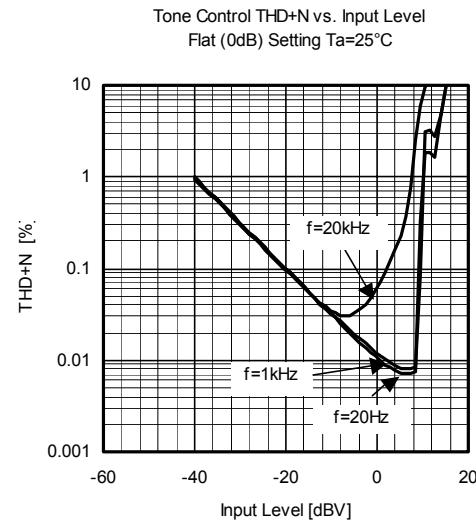
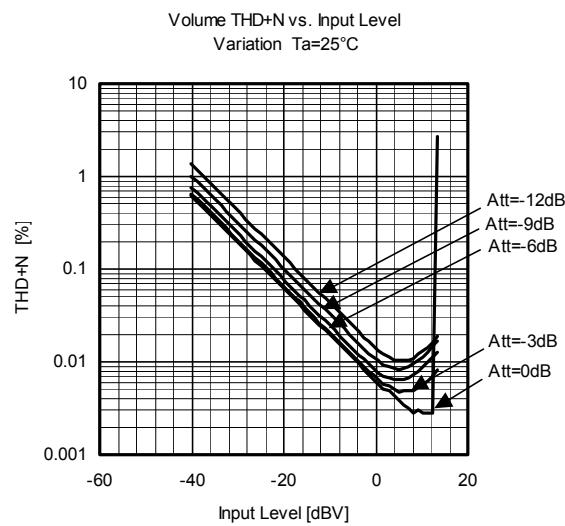
Select Address	BIT								REMARKS	
	D7	D6	D5	D4	D3	D2	D1	D0		
07H	TREBLE				BASS				TONE CONTROL Ex.) "11011101"=+10dB "11001100"=+8dB ": "10001000"=0dB "00000000"=0dB ": "01000111"=-8dB "01010101"=-10dB	
	1	1	0	1	1	1	0	1		
	1	1	0	0	1	1	0	0		
					:					
	1	0	0	0	1	0	0	0		
	0	0	0	0	0	0	0	0		
					:					
	0	1	0	0	0	1	0	0		
	0	1	0	1	0	1	0	1		
	*	MUTE								
08H	*	0								
		1								

NJW1150

■ TYPICAL CHARACTERISTICS

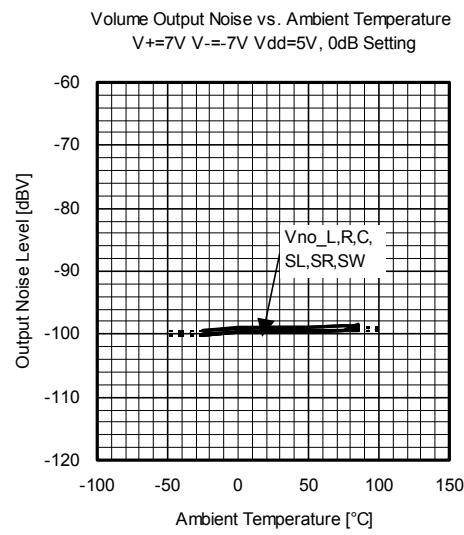
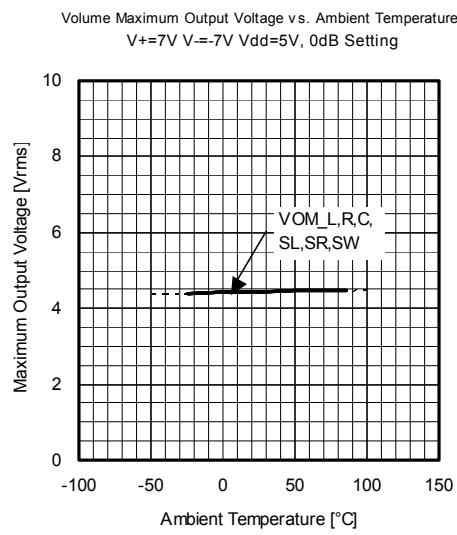
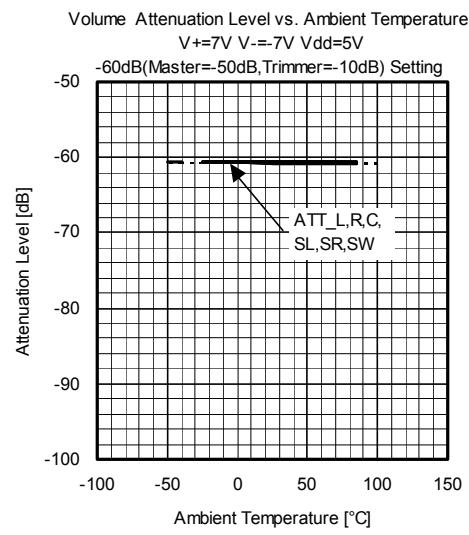
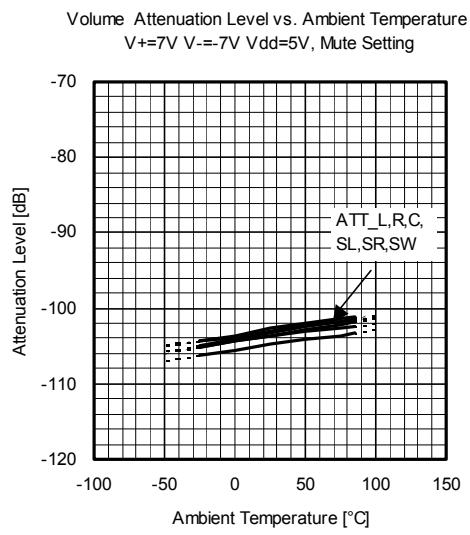
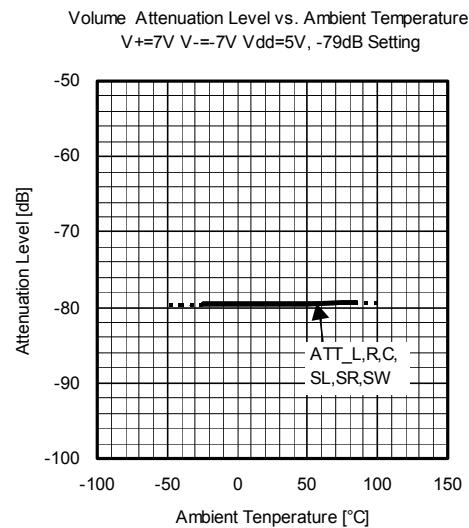
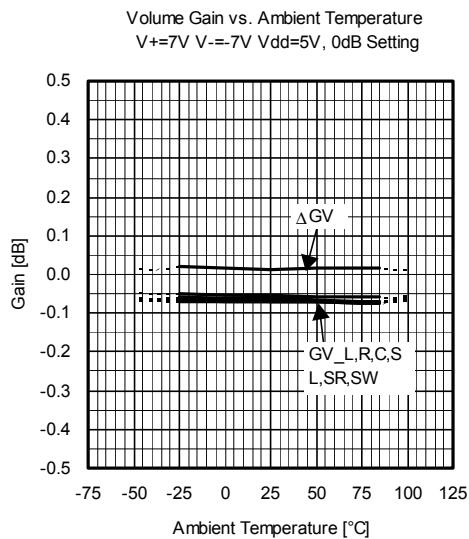


■ TYPICAL CHARACTERISTICS

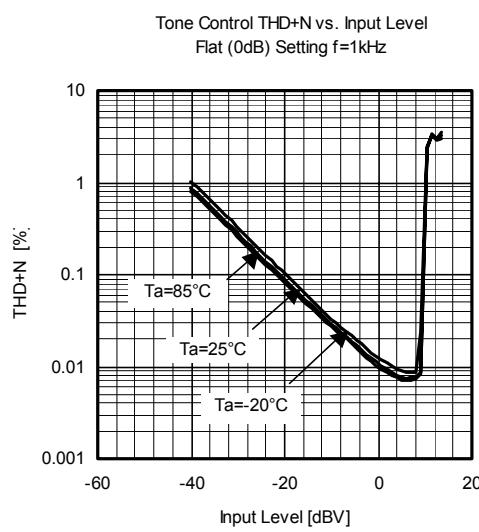
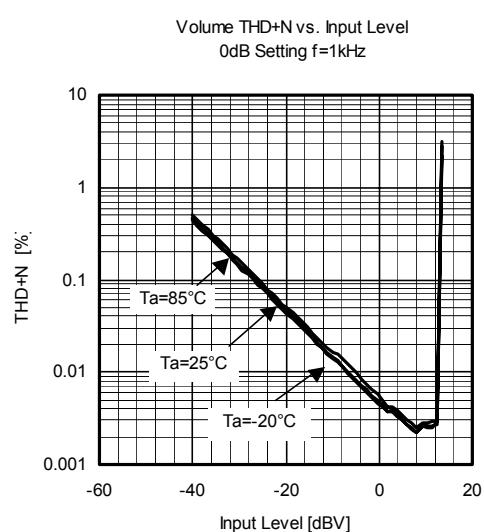
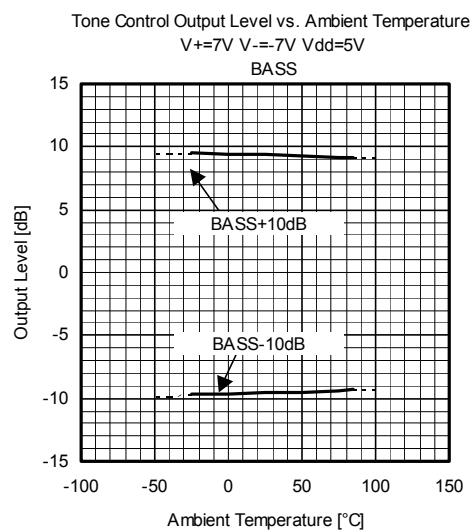
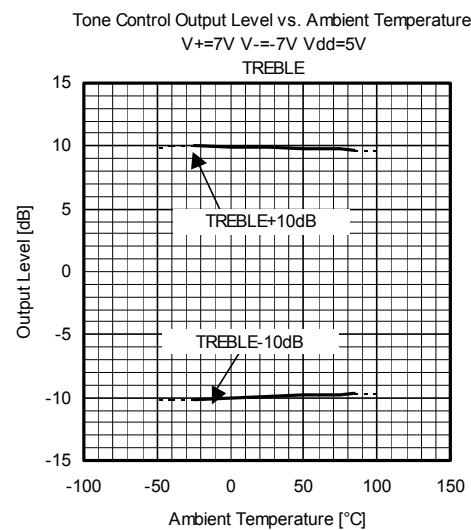
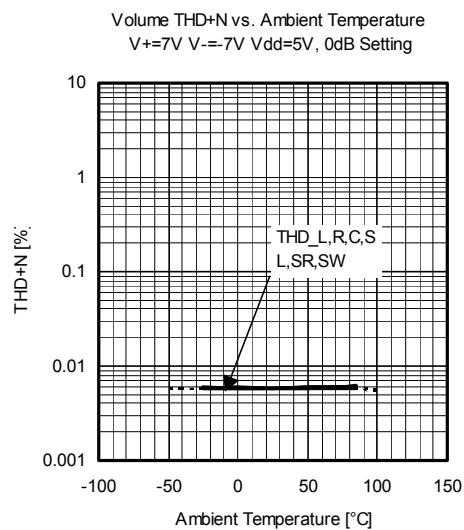


NJW1150

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS



[CAUTION]

The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.