

#### **DUAL 20V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	Package	I <sub>D</sub> T <sub>A</sub> = +25°C (Notes 5 & 6)
20V	130m $\Omega$ @ V <sub>GS</sub> = 4.5V	MSOP-8	2.5A
200	150mΩ @ $V_{GS}$ = 2.7V	WISOF-6	2.3A

## Description

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(on)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

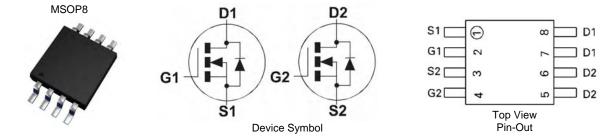
- DC-DC Converters
- Power Management functions
- Motor Control
- Disconnect Switches

#### Features

- Low On-Resistance
- Low Threshold
- Fast Switching Speed
- Low Gate Drive
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: MSOP-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish @3
- Weight: 0.008 grams (approximate)



#### Top View

### Ordering Information (Note 4)

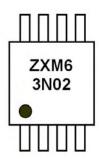
Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMD63N02XTA	ZXM63N02	7	12	1,000
ZXMD63N02XTC	ZXM63N02	13	12	4,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</li>

4. For packaging details, go to our website at http://www.diodes.com

### **Marking Information**



ZXM63N02 = Product type Marking Code



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic				Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V	
Gate-Source Voltage				V <sub>GSS</sub>	±12	V
Continuous Drain Current	Steady State		70°C (Note 5 & 6)	ID	2.5 1.9 0.78	А
Pulsed Drain Current (Notes 6 & 7)				I <sub>DM</sub>	19	А
Continuous Source Current (Body Diode) (No			(Notes 5 & 6)	ls	1.5	А
Pulsed Source Current (Body Diode) (Notes 6 & 7)			(Notes 6 & 7)	I <sub>SM</sub>	19	А

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
	(Notes 6 & 8)		0.87	
Power Dissipation	(Notes 5 & 6)	PD	1.25	W
	(Notes 8 & 9)		1.04	
	(Notes 6 & 8)		143	
Thermal Resistance, Junction to Ambient	(Notes 5 & 6)	R <sub>θJA</sub>	100	°C/W
	(Notes 8 & 9)		120	
Thermal Resistance, Junction to Leads	(Note 10)	R <sub>θJL</sub>	84.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

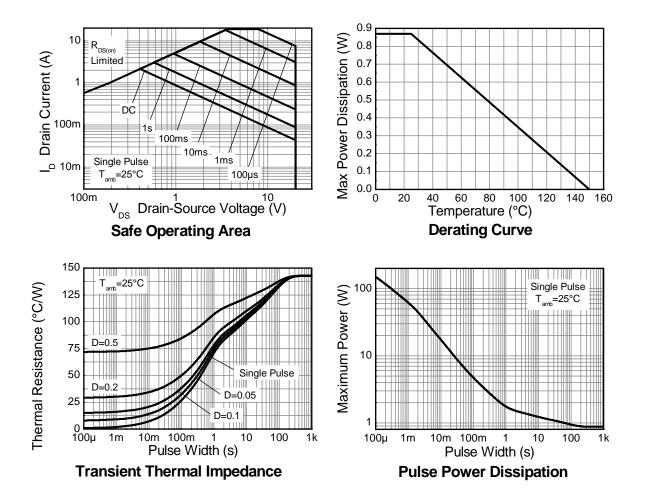
Notes: 5. For a device surface mounted on FR4 PCB measured at t  $\leq$  10 sec.

6. For device with one active die.

For device with one active due.
Repetitive rating - 25mm x 25mm FR4 PCB, D = 0.02, pulse width 300µs - pulse width limited by maximum junction temperature.
For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
For device with two active die running at equal power.
Thermal resistance from junction to solder-point (at the end of the drain lead).



# **Thermal Characteristics**





# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

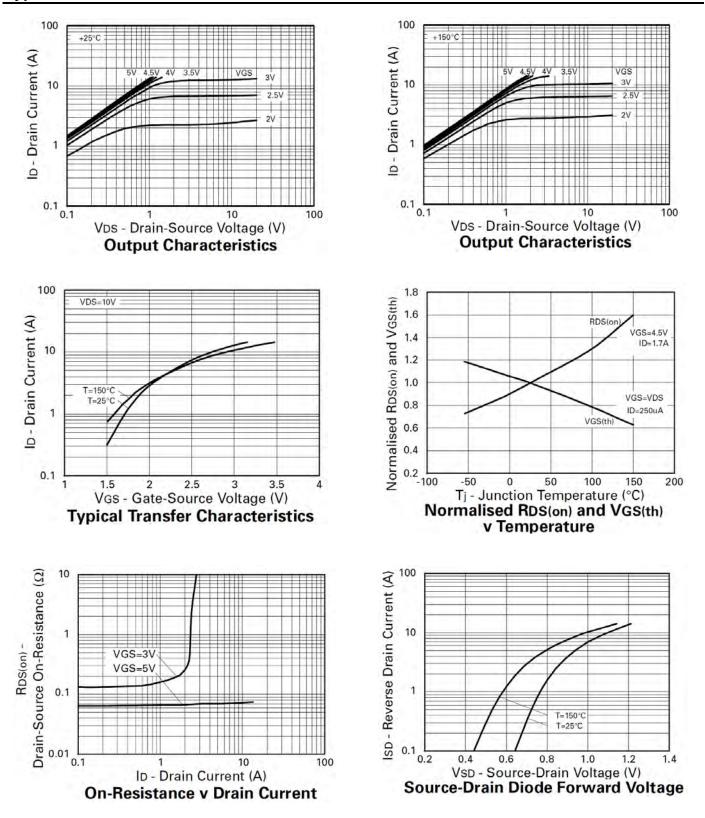
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS	Symbol	IVIIII	тур	IVIAX	Unit	Test condition	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>	-	-	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-	-	100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.7	-	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance (Note 11)			65	130	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1.7A	
Static Drain-Source On-Resistance (Note 11)	R <sub>DS (ON)</sub>	-	90	150	mΩ	$V_{GS} = 2.7V, I_D = 0.85A$	
Forward Transconductance (Notes 11 & 13)	g <sub>fs</sub>	2.6	-	-	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.85A	
Diodes Forward Voltage (Note 11)	V <sub>SD</sub>	-	0.85	0.95	V	$T_J = 25^{\circ}C, I_S = 1.7A, V_{GS} = 0V$	
DYNAMIC CHARACTERISTICS						-	
Input Capacitance (Note 12 & 13)	Ciss	-	350	700			
Output Capacitance (Notes 12 & 13)	C <sub>oss</sub>	-	120	250	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Reverse Transfer Capacitance (Notes 12 & 13)	C <sub>rss</sub>	-	50	100		I = 1.0MHz	
Gate Resistance (Notes 12 & 13)	Rq	-	3.8	7.6	Ω	$f = 1MHz, V_{GS} = 0V, V_{DS} = 0V$	
Total Gate Charge (Notes 12 & 13)	Qq	-	4.5	6			
Gate-Source Charge (Notes 12 & 13)	Q <sub>gs</sub>	-	0.5	0.65	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 16V, I <sub>D</sub> = 1.7A	
Gate-Drain Charge (Notes 12 & 13)	Q <sub>gd</sub>	-	2	2.5		ID = 1.7 A	
Reverse Recovery Time (Note 13)	t <sub>rr</sub>	-	15	30	ns	T <sub>.1</sub> = +25°C, I <sub>F</sub> = 1.7A,	
Reverse Recovery Charge (Note 13)	Q <sub>rr</sub>	-	5.9	-	nC	di/dt = 100A/µs	
Turn-On Delay Time (Notes 12 & 13)	t <sub>D(on)</sub>	-	3.4	-			
Turn-On Rise Time (Notes 12 & 13)	tr	-	8.1	-		$V_{DD} = 10V, I_D = 1.7A,$	
Turn-Off Delay Time (Notes 12 & 13)	t <sub>D(off)</sub>	-	13.5	-	ns	$R_G = 6\Omega, R_D = 5.7\Omega,$	
Turn-Off Fall Time (Notes 12 & 13)	tf	-	9.1	-	1		

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤2%.
Switching characteristics are independent of operating junction temperature.
For design aid only, not subject to production testing.

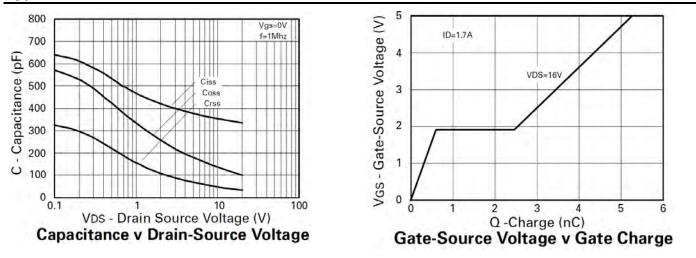


## **Typical Characteristics**

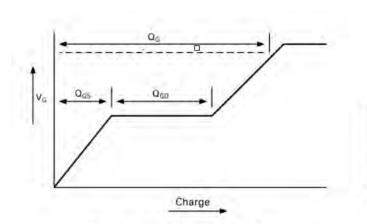




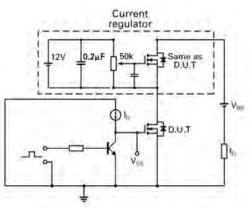
#### Typical Characteristics (cont.)



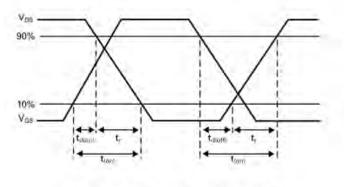
**Test Circuits** 



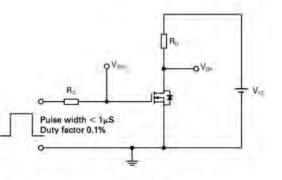
Basic gate charge waveform

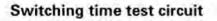


Gate charge test circuit



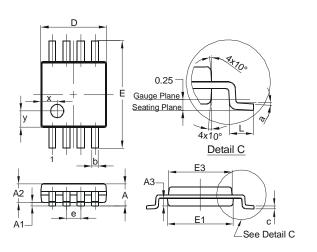
Switching time waveforms





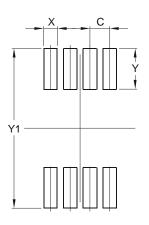


# **Package Outline Dimensions**



MSOP-8						
Dim	Min	Max	Тур			
Α	-	1.10	-			
A1	0.05	0.15	0.10			
A2	0.75	0.95	0.86			
A3	0.29	0.49	0.39			
b	0.22	0.38	0.30			
с	0.08	0.23	0.15			
D	2.90	3.10	3.00			
ш	4.70	5.10	4.90			
E1	2.90	3.10	3.00			
E3	2.85	3.05	2.95			
e	1	1	0.65			
_	0.40	0.80	0.60			
a	0°	8°	4°			
X	-	-	0.750			
у	-	-	0.750			
All Dimensions in mm						

# Suggested Pad Layout



Dimensions	Value (in mm)			
С	0.650			
Х	0.450			
Y	1.350			
Y1	5.300			



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