MOSFET - Power, Single N-Channel, SUPERFET®, FAST, TO220

600 V, 125 mΩ, 22 A

NTP125N60S5H

$V_{(BR)DSS}$	$R_{DS(ON)}$ MAX	I_D MAX
600 V	125 mΩ @ 10 V	22 A

Description

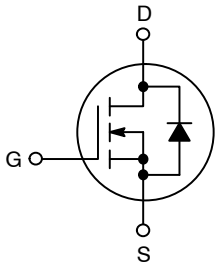
The SUPERFET V MOSFET FAST series helps maximize system efficiency by the extremely low switching losses in hard switching application.

Features

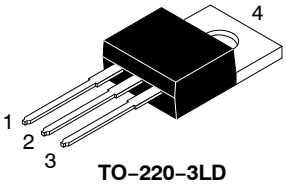
- 650 V @ T_J = 150°C / Typ. $R_{DS(on)}$ = 100 mΩ
- 100% Avalanche Tested
- Pb−Free, Halogen Free / BFR Free and RoHS Compliant

Applications

- Telecom / Server Power Supplies
- EV Charger / UPS / Solar / Industrial Power Supplies



N−CHANNEL MOSFET



TO−220−3LD
CASE 340AT

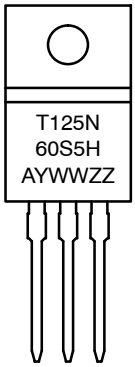
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain−to−Source Voltage		V_{DSS}	600	V
Gate−to−Source Voltage	DC	V_{GS}	±30	V
	AC (f > 1 Hz)		±30	
Continuous Drain Current	T_C = 25°C	I_D	22	A
	T_C = 100°C		13	
Power Dissipation	T_C = 25°C	P_D	152	W
Pulsed Drain Current (Note 1)	T_C = 25°C	I_{DM}	77	A
Pulsed Source Current (Body Diode) (Note 1)		I_{SM}	77	A
Operating Junction and Storage Temperature Range		T_J, T_{stg}	−55 to +150	°C
Source Current (Body Diode)		I_S	22	A
Single Pulse Avalanche Energy	I_L = 4.5 A, R_G = 25 Ω	E_{AS}	184	mJ
Avalanche Current		I_{AS}	4.5	A
Repetitive Avalanche Energy (Note 1)		E_{AR}	1.52	mJ
MOSFET dv/dt		dv/dt	120	V/ns
Peak Diode Recovery dv/dt (Note 2)			20	
Lead Temperature for Soldering Purposes (1/8″ from case for 10 seconds)		T_L	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse−width limited by maximum junction temperature.
2. I_{SD} ≤ 11 A, di/dt ≤ 200 A/μs, V_{DD} ≤ 400 V, starting T_J = 25°C.

MARKING DIAGRAM



T125N60S5H = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
NTP125N60S5H	TO220	50 Units / Tube

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction−to−Case	$R_{\theta JC}$	0.82	°C/W
Thermal Resistance, Junction−to−Ambient	$R_{\theta JA}$	62.5	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain−to−Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0 V, I_D = 10 mA, T_J = 25°C	600	–	–	V
Drain−to−Source Breakdown Voltage Temperature Coefficient	$\Delta V_{(BR)DSS}/\Delta T_J$	I_D = 10 mA, Referenced to 25°C	–	630	–	mV/°C
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS} = 0 V, V_{DS} = 600 V, T_J = 25°C	–	–	1	μA
Gate−to−Source Leakage Current	I_{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	–	–	±100	nA
ON CHARACTERISTICS						
Drain−to−Source On Resistance	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 11 A, T_J = 25°C	–	100	125	mΩ
Gate Threshold Voltage	$V_{GS(TH)}$	V_{GS} = V_{DS}, I_D = 2.1 mA, T_J = 25°C	2.7	–	4.3	V
Forward Transconductance	g_{FS}	V_{DS} = 20 V, I_D = 11 A	–	21.7	–	S
CHARGES, CAPACITANCES & GATE RESISTANCE						
Input Capacitance	C_{ISS}	V_{DS} = 400 V, V_{GS} = 0 V, f = 250 kHz	–	2036	–	pF
Output Capacitance	C_{OSS}		–	31.4	–	
Time Related Output Capacitance	$C_{OSS(tr)}$	I_D = Constant, V_{DS} = 0 to 400 V, V_{GS} = 0 V	–	485	–	
Energy Related Output Capacitance	$C_{OSS(er)}$	V_{DS} = 0 to 400 V, V_{GS} = 0 V	–	52.5	–	
Total Gate Charge	$Q_{G(TOT)}$	V_{DD} = 400 V, I_D = 11 A, V_{GS} = 10 V	–	37.3	–	nC
Gate−to−Source Charge	Q_{GS}		–	9.92	–	
Gate−to−Drain Charge	Q_{GD}		–	10.4	–	
Gate Resistance	R_G	f = 1 MHz	–	1.08	–	Ω
SWITCHING CHARACTERISTICS						
Turn−On Delay Time	$t_{d(ON)}$	V_{GS} = 0/10 V, V_{DD} = 400 V, I_D = 11 A, R_G = 7.5 Ω	–	21	–	ns
Rise Time	t_r		–	6.02	–	
Turn−Off Delay Time	$t_{d(OFF)}$		–	59.8	–	
Fall Time	t_f		–	2.66	–	
SOURCE−TO−DRAIN DIODE CHARACTERISTICS						
Forward Diode Voltage	V_{SD}	I_{SD} = 11 A, V_{GS} = 0 V, T_J = 25°C	–	–	1.2	V
Reverse Recovery Time	t_{RR}	V_{GS} = 0 V, I_{SD} = 11 A, dI/dt = 100 A/μs, V_{DD} = 400 V	–	337	–	ns
Reverse Recovery Charge	Q_{RR}		–	4529	–	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

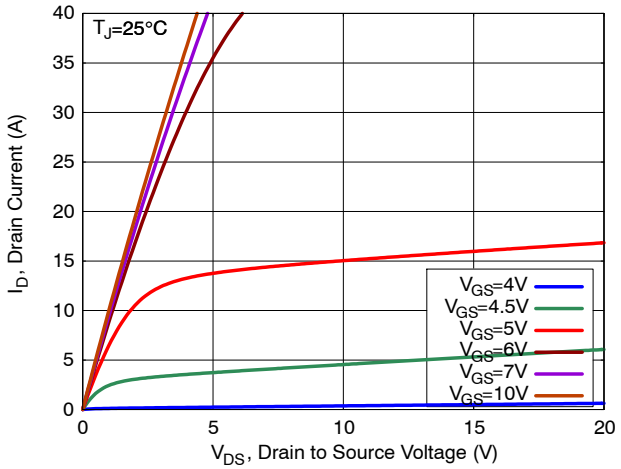


Figure 1. On−Region Characteristics

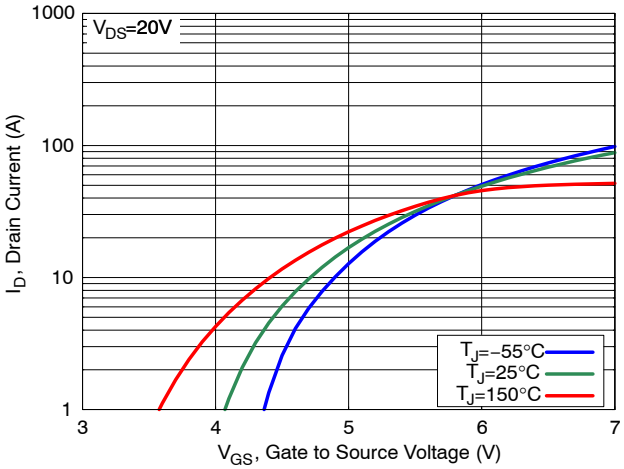


Figure 2. Transfer Characteristics

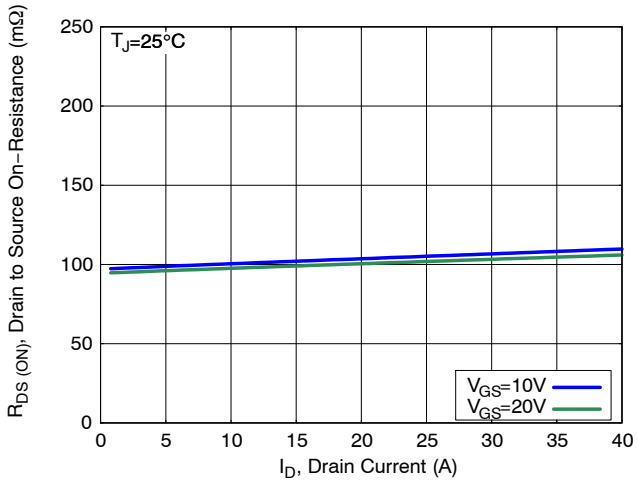


Figure 3. On−Resistance Variation vs. Drain Current and Gate Voltage

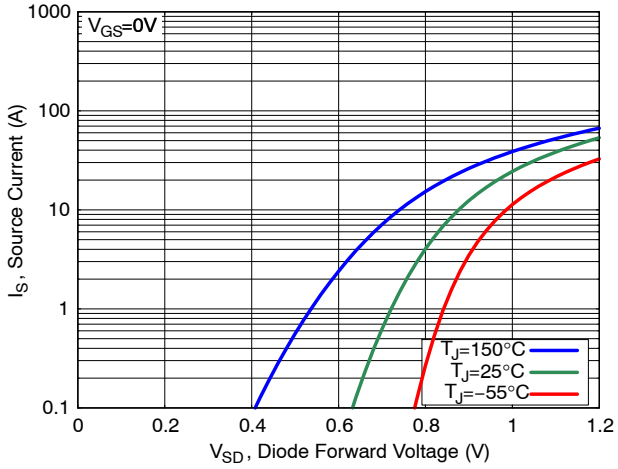


Figure 4. Diode Forward Voltage vs. Source Current

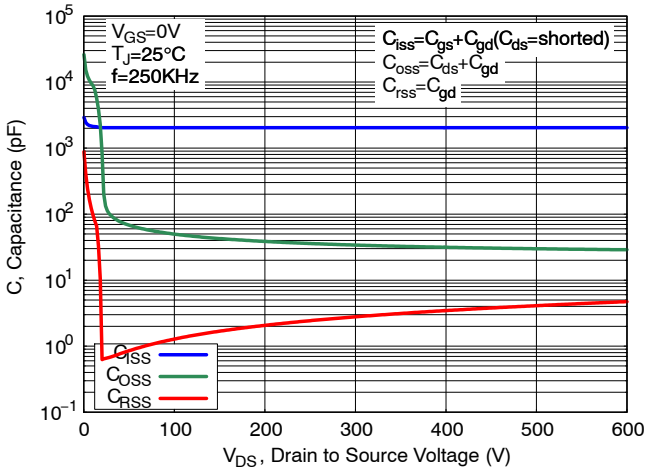


Figure 5. Capacitance Characteristics

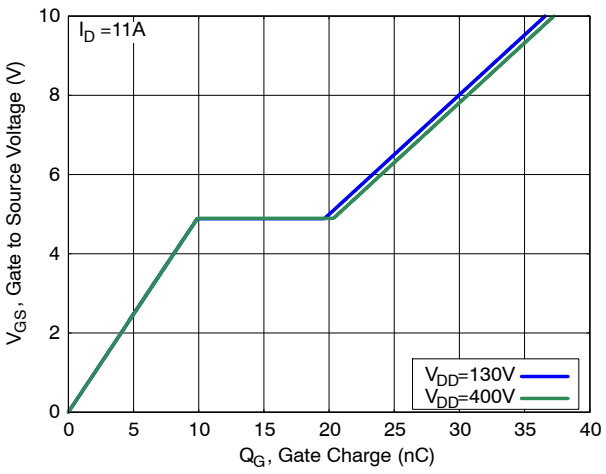


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS

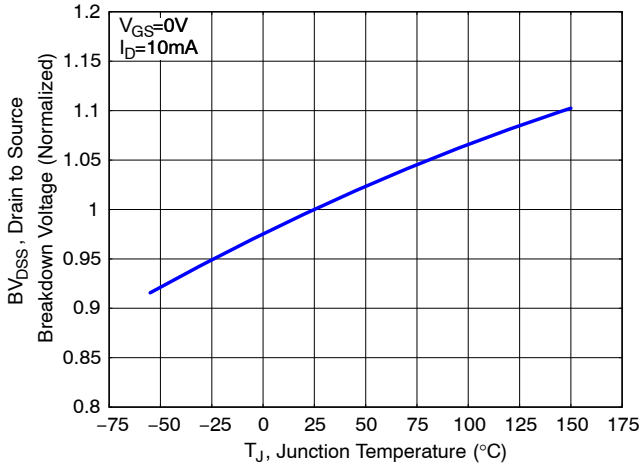


Figure 7. Breakdown Voltage Variation vs. Temperature

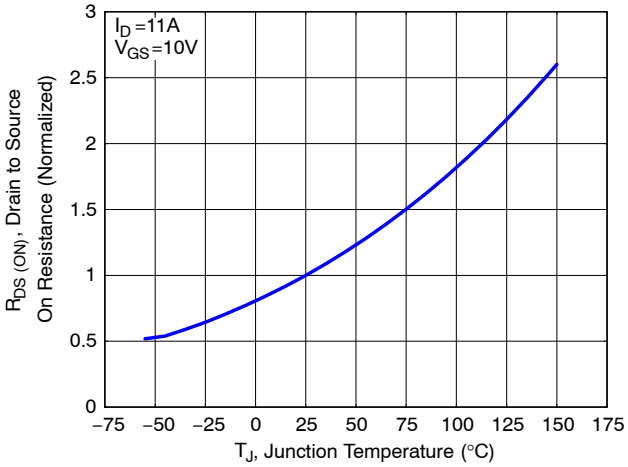


Figure 8. On−Resistance Variation vs. Temperature

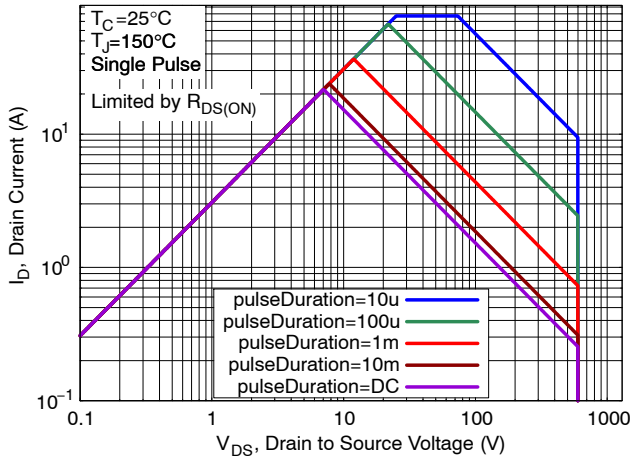


Figure 9. Maximum Safe Operating Area

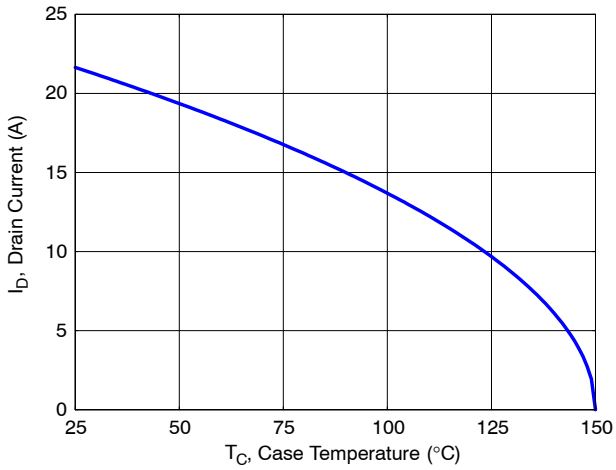


Figure 10. Maximum Drain Current vs. Case Temperature

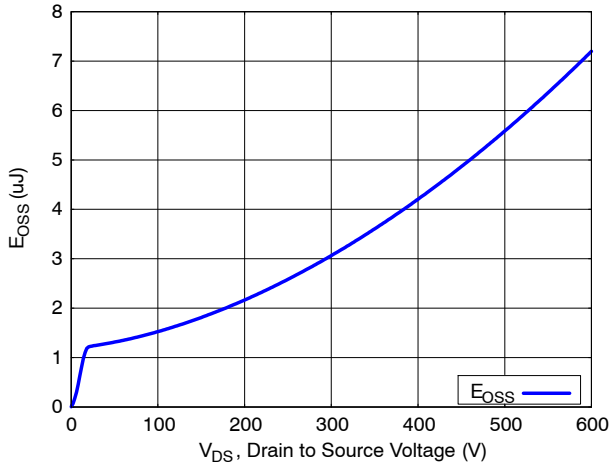


Figure 11. Eoss vs. Drain−to−Source Voltage

TYPICAL CHARACTERISTICS

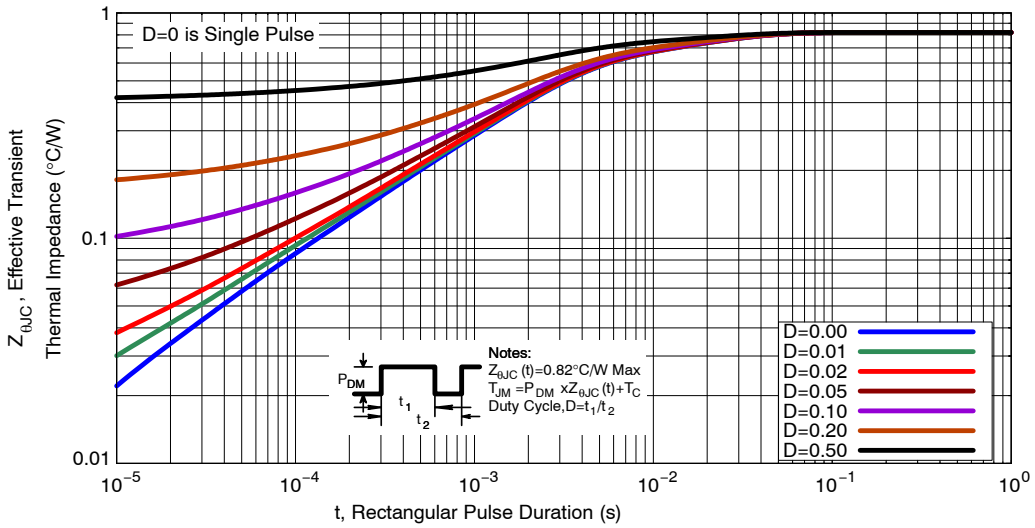
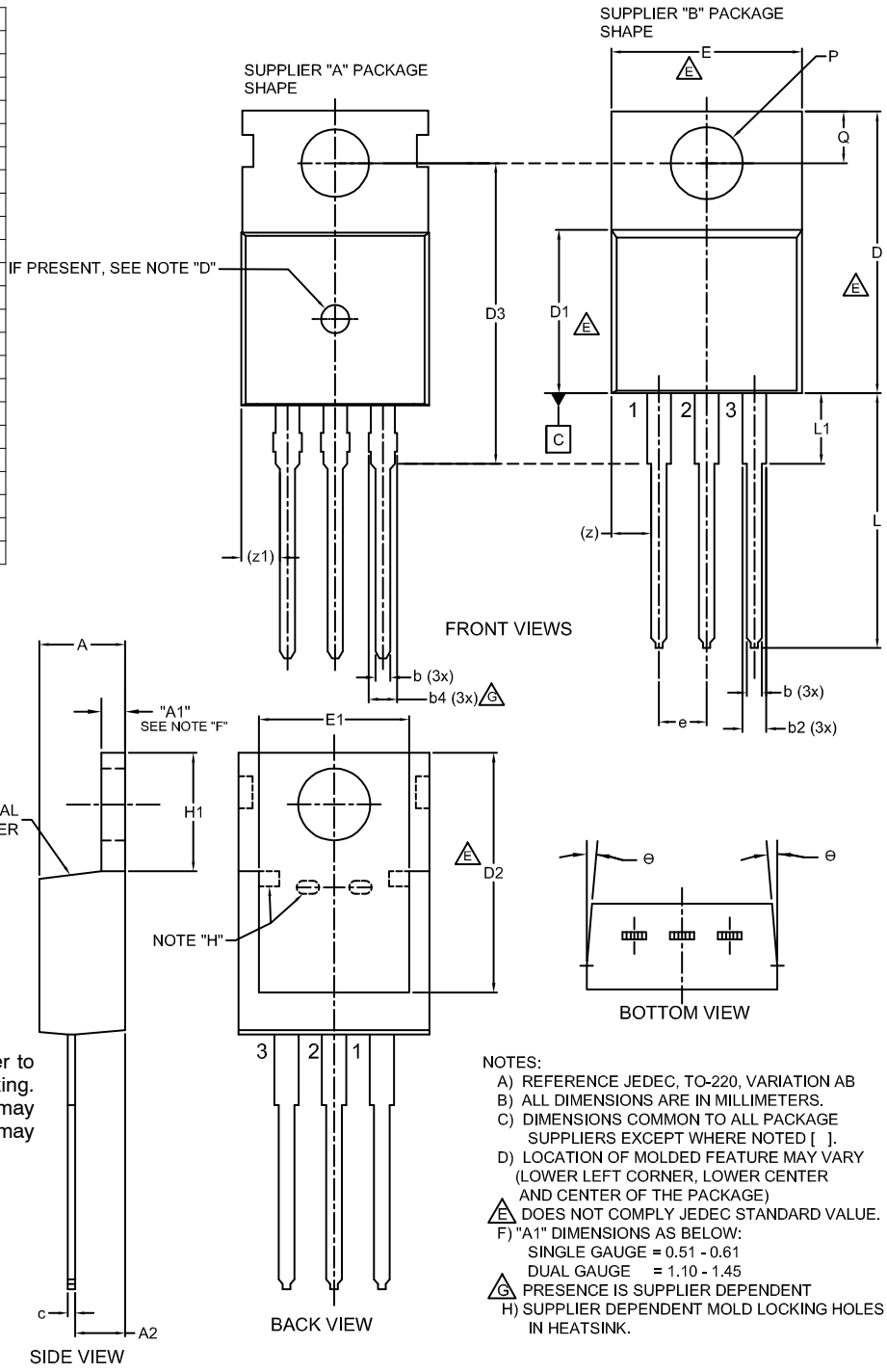


Figure 12. Transient Thermal Impedance

PACKAGE DIMENSIONS

TO−220−3LD
CASE 340AT
ISSUE B

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.00	--	4.70
A1	SEE NOTE "F"		
A2	2.10	--	2.85
b	0.55	--	1.00
b2	1.10	--	1.62
b4	1.42	--	1.62
c	0.36	--	0.60
D	13.90	--	16.30
D1	8.13	--	9.40
D2	11.50	--	14.30
D3	15.42	--	16.51
E	9.65	--	10.67
E1	7.59	--	8.65
e	2.40	--	2.67
H1	6.06	--	6.69
L	12.70	--	14.04
L1	2.70	--	4.10
P	3.50	--	4.00
Q	2.50	--	3.40
z	2.13 REF		
z1	2.06 REF		
Θ	3°	–	5°



GENERIC MARKING DIAGRAM*

XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

NOTES:
A) REFERENCE JEDEC, TO-220, VARIATION AB
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [ ].
D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
E) DOES NOT COMPLY JEDEC STANDARD VALUE.
F) "A1" DIMENSIONS AS BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1.10 - 1.45
G) PRESENCE IS SUPPLIER DEPENDENT
H) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Email Requests to: orderlit@onsemi.com

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For additional information, please contact your local Sales Representative

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