# **MOSFET** - Power, Single

# **N-Channel**

80 V, 3.2 mΩ, 135 A

## NTMFS6H818NL

#### **Features**

- Small Footprint (5x6 mm) for Compact Design
- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Q<sub>G</sub> and Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	80	V
Gate-to-Source Voltage	9		V <sub>GS</sub>	±20	V
Continuous Drain		T <sub>C</sub> = 25°C	I <sub>D</sub>	135	Α
Current R <sub>θJC</sub> (Notes 1, 3)	Steady	T <sub>C</sub> = 100°C		95	
Power Dissipation	State	T <sub>C</sub> = 25°C	$P_{D}$	140	W
R <sub>θJC</sub> (Note 1)		T <sub>C</sub> = 100°C		70	
Continuous Drain		T <sub>A</sub> = 25°C	I <sub>D</sub>	22	Α
Current R <sub>0JA</sub> (Notes 1, 2, 3)	State T <sub>A</sub> = 25°	T <sub>A</sub> = 100°C		16	
Power Dissipation		T <sub>A</sub> = 25°C	$P_{D}$	3.8	W
R <sub>θJA</sub> (Notes 1, 2)		T <sub>A</sub> = 100°C		1.9	
Pulsed Drain Current	$T_A = 25^\circ$	°C, t <sub>p</sub> = 10 μs	I <sub>DM</sub>	772	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Source Current (Body Diode)			I <sub>S</sub>	116	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 9.3 A)			E <sub>AS</sub>	707	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	1.1	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	39	

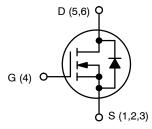
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using a 650 mm<sup>2</sup>, 2 oz. Cu pad.
- Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.



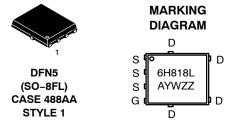
#### ON Semiconductor®

#### www.onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX	
80 V	3.2 m $\Omega$ @ 10 V	135 A	
	4.1 mΩ @ 4.5 V	135 A	



**N-CHANNEL MOSFET** 



A = Assembly Location

Y = Year

W = Work Week
ZZ = Lot Traceability

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		80			٧
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /				44.6		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$I_{DSS}$ $V_{GS} = 0 \text{ V},$ $T_{J} = 25 ^{\circ}\text{C}$				10	
		$V_{DS} = 80 \text{ V}$	T <sub>J</sub> = 125°C			100	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 20 V				100	nA
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 190 μA		1.2		2.0	V
Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				-5.3		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A		2.7	3.2	mΩ
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 20 A		3.3	4.1	mΩ
Forward Transconductance	9FS	V <sub>DS</sub> = 8 V, I <sub>[</sub>	<sub>O</sub> = 50 A		200		S
CHARGES, CAPACITANCES & GATE RES	SISTANCE						
Input Capacitance	C <sub>ISS</sub>				3844		
Output Capacitance	C <sub>OSS</sub>	$V_{GS} = 0 \text{ V, } f = 1 \text{ MHz, } V_{DS} = 40 \text{ V}$ $V_{GS} = 10 \text{ V, } V_{DS} = 40 \text{ V; } I_{D} = 50 \text{ A}$			484		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>				21		
Total Gate Charge	Q <sub>G(TOT)</sub>				64		
Threshold Gate Charge	Q <sub>G(TH)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 40 V; I <sub>D</sub> = 50 A			6		1
Gate-to-Source Charge	Q <sub>GS</sub>				11		nC
Gate-to-Drain Charge	Q <sub>GD</sub>				11.2		
Plateau Voltage	$V_{GP}$				3		V
Total Gate Charge	Q <sub>G(TOT)</sub>				31		nC
SWITCHING CHARACTERISTICS (Note 5)							
Turn-On Delay Time	t <sub>d(ON)</sub>				22		
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 4.5 V. V	ns = 64 V.		106		ns
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$V_{GS} = 4.5 \text{ V}, V_{I}$ $I_{D} = 50 \text{ A}, R_{G}$	$_{\rm i}$ = 2.5 $\Omega$		39		
Fall Time	t <sub>f</sub>				13		1
DRAIN-SOURCE DIODE CHARACTERIST	rics				•		
Forward Diode Voltage	$V_{SD}$	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 20 A	T <sub>J</sub> = 25°C		0.77	1.2	V
			T <sub>J</sub> = 125°C		0.63		
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 50 \text{ A}$			59		
Charge Time	t <sub>a</sub>				33		ns
Discharge Time	t <sub>b</sub>				25		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.

5. Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**

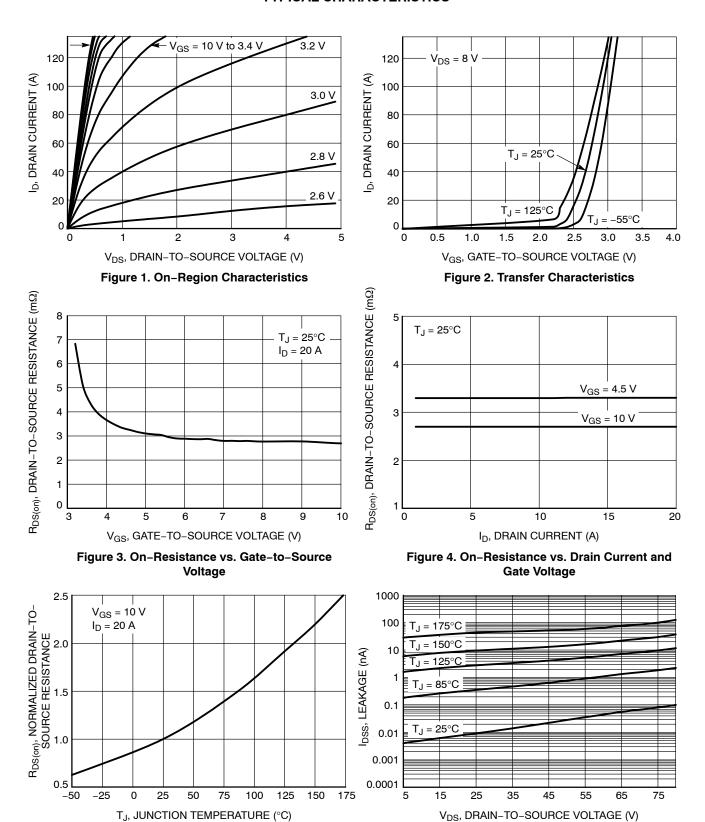


Figure 6. Drain-to-Source Leakage Current

vs. Voltage

Figure 5. On-Resistance Variation with

**Temperature** 

#### **TYPICAL CHARACTERISTICS**

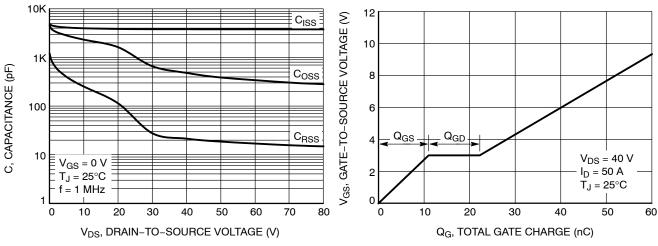
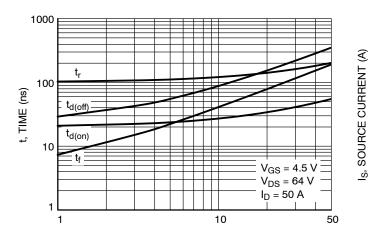


Figure 7. Capacitance Variation



 $\label{eq:RG} \textbf{R}_{\textbf{G}}, \, \textbf{GATE RESISTANCE} \; (\Omega)$  Figure 9. Resistive Switching Time Variation vs. Gate Resistance

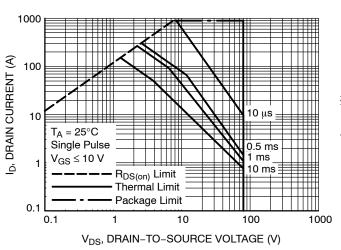


Figure 11. Safe Operating Area

Figure 8. Gate-to-Source Voltage vs. Total Charge

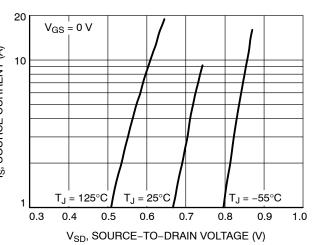


Figure 10. Diode Forward Voltage vs. Current

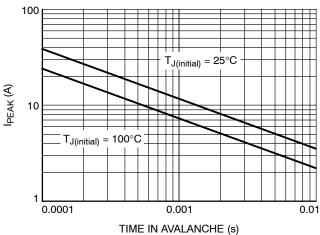


Figure 12. Maximum Drain Current vs. Time in Avalanche

#### **TYPICAL CHARACTERISTICS**

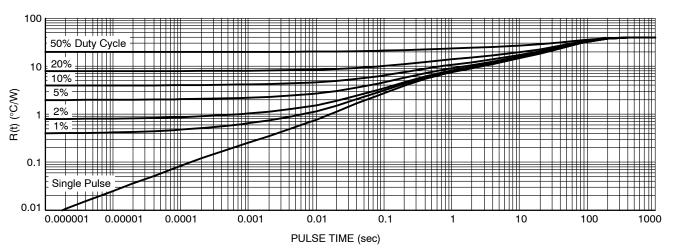


Figure 13. Thermal Response

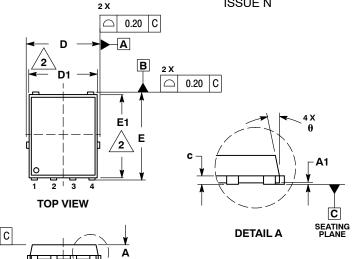
#### **DEVICE ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>
NTMFS6H818NLT1G	6H818L	DFN5 (Pb-Free)	1500 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS



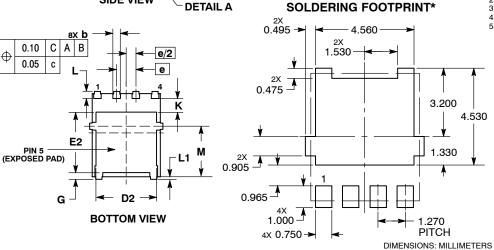


- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE

	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.90	1.00	1.10	
A1	0.00		0.05	
b	0.33	0.41	0.51	
С	0.23	0.28	0.33	
D	5.00	5.15	5.30	
D1	4.70	4.90	5.10	
D2	3.80	4.00	4.20	
E	6.00	6.15	6.30	
E1	5.70	5.90	6.10	
E2	3.45	3.65	3.85	
е	1.27 BSC			
G	0.51	0.575	0.71	
K	1.20	1.35	1.50	
L	0.51	0.575	0.71	
L1	0.125 REF			
M	3.00	3.40	3.80	
θ	0 °		12 °	

- STYLE 1: PIN 1. SOURCE 2. SOURCE 3. SOURCE

  - GATE
  - 5 DRAIN



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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