MOSFET - Power, N-Channel, SUPERFET® V, FAST

600 V, 185 mΩ, 15 A

NTHL185N60S5H

Description

The SUPERFET V MOSFET is the fifth generation high voltage super–junction (SJ) MOSFET family from ON Semiconductor. SUPERFET V delivers best–in–class FOMs $(R_{DS(ON)}\cdot Q_G$ and $R_{DS(ON)}\cdot E_{OSS})$ to improve not only heavy load but also light load efficiency. The 600 V SUPERET V series provides design benefits through reduced conduction and switching losses, while supporting extreme MOSFET dV_DS/dt ratings at 120 V/ns. Consequently, the SUPERFET V MOSFET FAST series helps maximize system efficiency and power density.

Features

- 650 V @ $T_I = 150$ °C
- Typ. $R_{DS(on)} = 148 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 25 nC)
- Low Time Related Output Capacitance (Typ. C_{oss(tr.)} = 372 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

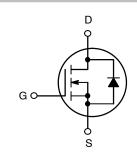
- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar

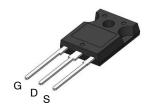


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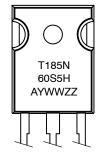
V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
600 V	185 mΩ @ 10 V	15 A	





TO-247 Long Leads CASE 340CX

MARKING DIAGRAM



T185N60S5H = Specific Device Code
A = Assembly Plant Code
YWW = Date Code (Year & Week)
ZZ = Lot

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Parameter	Value	Unit	
V_{DSS}	Drain to Source Voltage		600	V
V_{GSS}	Gate to Source Voltage	- DC	±30	V
		- AC (f > 1 Hz)	±30	
I _D	Drain Current	– Continuous (T _C = 25°C)	15	А
		- Continuous (T _C = 100°C)	9	
I _{DM}	Drain Current	- Pulsed (Note 1)	53	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	124	mJ	
I _{AS}	Avalanche Current (Note 2)	3.6	Α	
E _{AR}	Repetitive Avalanche Energy (Note 1)	1.16	mJ	
dv/dt	MOSFET dv/dt	120	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		20	
P_{D}	Power Dissipation	(T _C = 25°C)	116	W
		- Derate Above 25°C	0.93	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. Repetitive rating: pulse–width limited by maximum junction temperature.
2. $I_{AS} = 3.6 \text{ A}$, $R_{G} = 25 \Omega$, starting $T_{J} = 25^{\circ}\text{C}$.
3. $I_{SD} \le 7.5 \text{ A}$, di/dt $\le 200 \text{ A}/\mu\text{s}$, $V_{DD} \le 400 \text{ V}$, starting $T_{J} = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ hetaJC}$	Thermal Resistance, Junction to Case, Max.	1.08	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient, Max.	40	

PACKAGE MARKING AND ORDERING INFORMATION

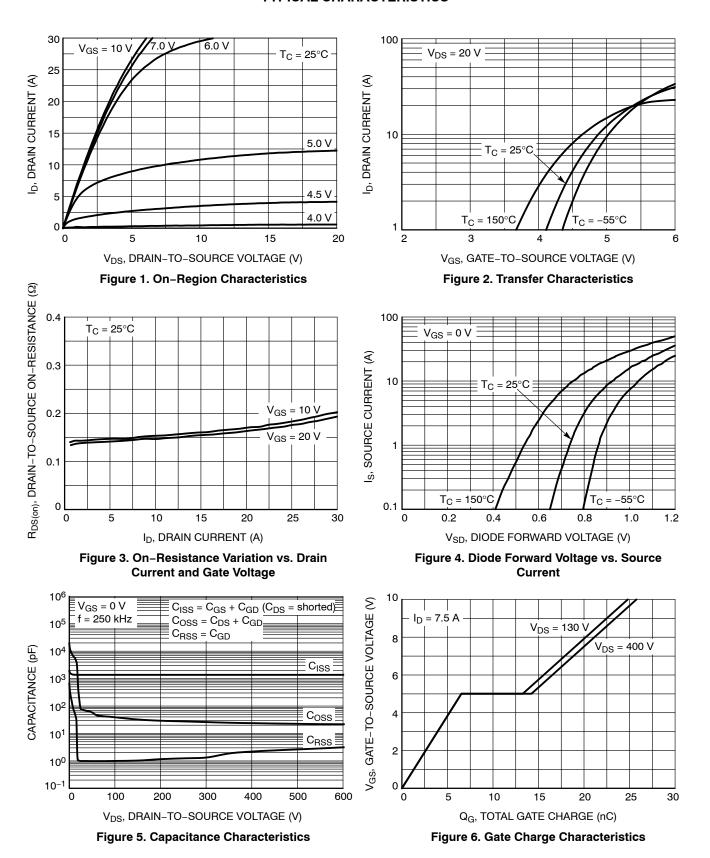
Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTHL185N60S5H	T185N60S5H	TO-247	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS		•		•	
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V, I}_{D} = 1 \text{ mA, T}_{J} = 25^{\circ}\text{C}$	600	-	_	V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	650	-	_	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, Referenced to 25°C	-	0.63	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V	_	-	1	μΑ
		V _{DS} = 480 V, T _C = 125°C	-	0.69	_	1
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	-	±100	nA
ON CHARACTE	RISTICS		•	•		
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1.4 \text{ mA}$	2.7	_	4.3	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 7.5 A	-	148	185	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 7.5 A	-	18	_	S
YNAMIC CHA	RACTERISTICS		•		-	
C _{iss}	Input Capacitance		_	1350	_	pF
C _{oss}	Output Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 250 \text{ kHz}$	_	25	_	pF
C _{oss(tr.)}	Time Related Output Capacitance	I_D = Constant, V_{DS} = 0 V to 400 V, V_{GS} = 0 V	-	372	-	pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	_	42	_	pF
Q _{g(tot)}	Total Gate Charge		_	25	_	nC
Q _{gs}	Gate to Source Charge	V_{DD} = 400 V, I_{D} = 7.5 A, V_{GS} = 10 V	_	7	-	nC
Q_{gd}	Gate to Drain Charge		_	8	_	nC
ESR	Equivalent Series Resistance	f = 1 MHz	_	0.9	_	Ω
WITCHING CH	IARACTERISTICS		•		-	
t _{d(on)}	Turn-On Delay Time		-	18	_	ns
t _r	Turn-On Rise Time	V _{DD} = 400 V, I _D = 7.5 A,	_	9	_	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_g = 10 \Omega$	_	53	_	ns
t _f	Turn-Off Fall Time		_	4	_	ns
SOURCE-DRAII	N DIODE CHARACTERISTICS					
IS	Maximum Continuous Source to Drain Diode Forward Current		-	_	15	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current		_	-	53	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 7.5 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 7.5 A,	_	251	-	ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs	_	3	_	μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

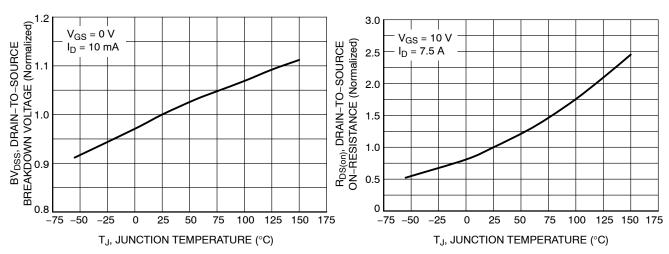
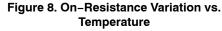


Figure 7. Breakdown Voltage Variation vs. Temperature



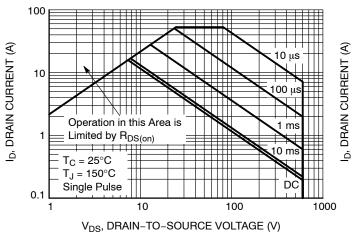


Figure 9. Maximum Safe Operating Area

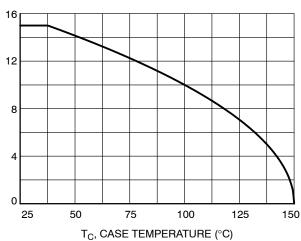


Figure 10. Maximum Drain Current vs. Case Temperature

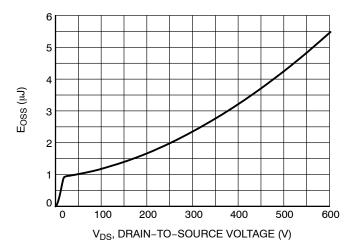


Figure 11. E_{OSS} vs. Drain-to-Source Voltage

TYPICAL CHARACTERISTICS

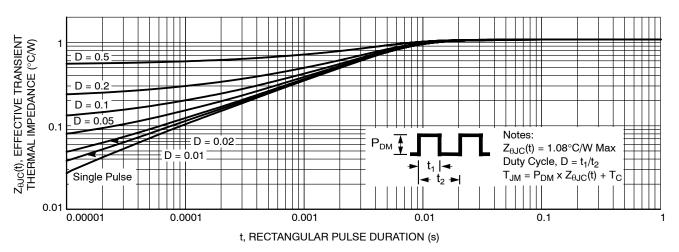


Figure 12. Transient Thermal Impedance

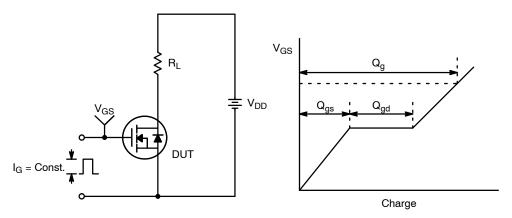


Figure 13. Gate Charge Test Circuit & Waveform

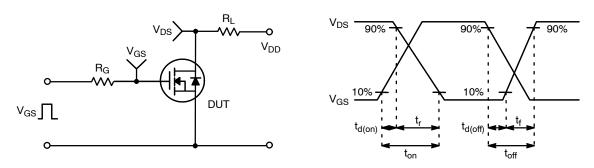


Figure 14. Resistive Switching Test Circuit & Waveforms

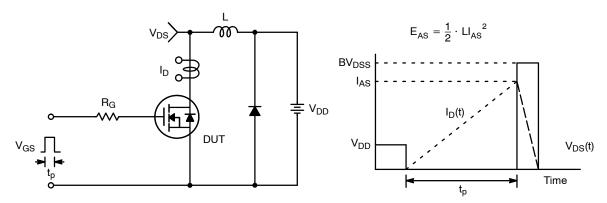


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

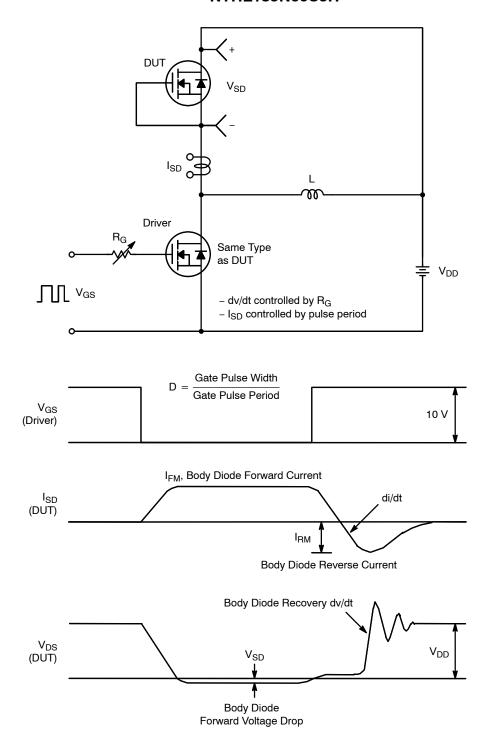
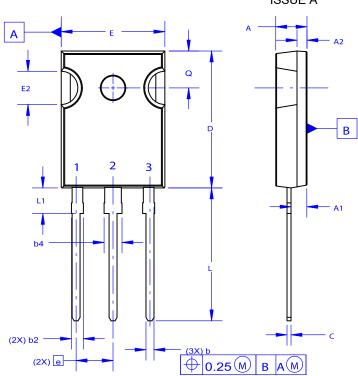


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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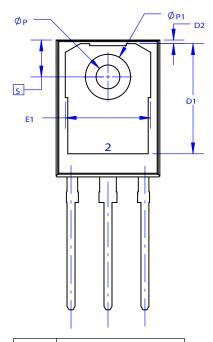
PACKAGE DIMENSIONS

TO-247-3LD CASE 340CX **ISSUE A**



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.



DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.58	4.70	4.82		
A1	2.20	2.40	2.60		
A2	1.40	1.50	1.60		
D	20.32	20.57	20.82		
Е	15.37	15.62	15.87		
E2	4.96	5.08	5.20		
е	~	5.56	~		
L	19.75	20.00	20.25		
L1	3.69	3.81	3.93		
ØΡ	3.51	3.58	3.65		
Q	5.34	5.46	5.58		
S	5.34	5.46	5.58		
b	1.17	1.26	1.35		
b2	1.53	1.65	1.77		
b4	2.42	2.54	2.66		
С	0.51	0.61	0.71		
D1	13.08	~	~		
D2	0.51	0.93	1.35		
E1	12.81	~	~		
ØP1	6.60	6.80	7.00		

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