

Silicon Carbide (SiC) MOSFET - EliteSiC, 29 mohm, 1200 V, M3S, TO-247-4L NTH4L030N120M3S

Features

- Typ. $R_{DS(on)} = 29 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge (Q_{G(tot)} = 107 nC)
- High Speed Switching with Low Capacitance (Coss = 106 pF)
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- Solar Inverters
- Electric Vehicle Charging Stations
- UPS (Uninterruptible Power Supplies)
- Energy Storage Systems
- SMPS (Switch Mode Power Supplies)

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage			V_{DSS}		
Gate-to-Source Voltage			V_{GS}	-10/+22	٧
Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-3/+18	>	
Continuous Drain Current (Notes 1, 3)	Steady T _C =25°C		I _D	73	Α
Power Dissipation (Note 1)			P _D	313	W
Continuous Drain Current (Notes 1, 3)	Steady State	T _C =100°C	I _D	52	Α
Power Dissipation (Note 1)			P _D	156	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	193	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode) T _C = 25°C, V _{GS} = -3 V			I _S	62	Α
Single Pulse Drain-to-Source Avalanche Energy (Note 4)			E _{AS}	220	mJ
Maximum Lead Temperature for Soldering (1/25" from case for 10 s)		TL	270	°C	

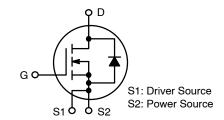
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

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- 2. Repetitive rating, limited by max junction temperature.
- 3. The maximum current rating is based on typical R_{DS(on)} performance.
- 4. EAS of 220 mJ is based on starting $T_J = 25$ °C; L = 1 mH, $I_{AS} = 21$ A, $V_{DD} = 100$ V, $V_{GS} = 18$ V.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
1200 V	39 mΩ @ 18 V	73 A

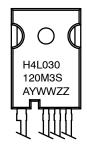


N-CHANNEL MOSFET



TO-247-4L CASE 340CJ

MARKING DIAGRAM



H4L030120M3S = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NTH4L030N120M3S	TO-247-4L	30 Units / Tube

Table 1. THERMAL CHARACTERISTICS

Parameter		Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	0.48	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{ heta JA}$	40	

Table 2. ELECTRICAL CHARACTERISTICS (T. J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF-STATE CHARACTERISTICS		1	•			•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	1200	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 1 mA, referenced to 25°C (Note 6)	-	0.3	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 1200 V	-	-	100	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +22/-10 \text{ V}, V_{DS} = 0 \text{ V}$	-	_	±1	μΑ
ON-STATE CHARACTERISTICS (Note 2)						
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 15 \text{ mA}$	2.04	2.4	4.4	V
Recommended Gate Voltage	V_{GOP}		-3	-	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = 18 \text{ V}, I_D = 30 \text{ A}, T_J = 25^{\circ}\text{C}$	-	29	39	mΩ
		V _{GS} = 18 V, I _D = 30 A, T _J = 175°C (Note 6)	-	58	-	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 30 A (Note 6)	-	30	-	S
CHARGES, CAPACITANCES & GATE RE	SISTANCE					
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 800 V	-	2430	-	pF
Output Capacitance	C _{OSS}		-	106	-	
Reverse Transfer Capacitance	C _{RSS}		_	9.4	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$ $I_{D} = 30 \text{ A}$	-	107	-	nC
Threshold Gate Charge	Q _{G(TH)}	I _D = 30 A	-	6	-	
Gate-to-Source Charge	Q _{GS}		-	17	-	
Gate-to-Drain Charge	Q_{GD}		_	28	-	
Gate-Resistance	R_{G}	f = 1 MHz	-	3.3	-	Ω
SWITCHING CHARACTERISTICS				•		
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$	-	13	-	ns
Rise Time	t _r	$I_D = 30 \text{ A}, R_G = 4.7 \Omega$ Inductive load (Notes 5, 6)	-	19	-	
Turn-Off Delay Time	t _{d(OFF)}	, , ,	-	48	-	1
Fall Time	t _f		-	11	-	
Turn-On Switching Loss	E _{ON}		-	324	_	μJ
Turn-Off Switching Loss	E _{OFF}		-	134	_	1
Total Switching Loss	E _{tot}		-	458	_	
SOURCE-DRAIN DIODE CHARACTERIS						
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -3 \text{ V}, T_C = 25^{\circ}\text{C (Note 6)}$	-	_	62	А
Pulsed Source–Drain Diode Forward Current (Note 2)	I _{SDM}		-	-	193	
Forward Diode Voltage	V _{SD}	V _{GS} = -3 V, I _{SD} = 30 A, T _J = 25°C	_	4.6	-	V

Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
SOURCE-DRAIN DIODE CHARACTEI	RISTICS				•	
Reverse Recovery Time	t _{RR}	$V_{GS} = -3/18 \text{ V, } I_{SD} = 30 \text{ A,}$ $dI_S/dt = 1000 \text{ A/}\mu\text{s, } V_{DS} = 800 \text{ V}$ (Note 6)	-	20	-	ns
Reverse Recovery Charge	Q _{RR}		-	114	-	nC
Reverse Recovery Energy	E _{REC}		-	10.5	-	μJ
Peak Reverse Recovery Current	I _{RRM}		-	11	_	Α
Charge Time	T _A		-	11	_	ns
Discharge Time	T _B	1	-	8.5	_	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. E_{ON}/E_{OFF} result is with body diode.

6. Defined by design, not subject to production test.

TYPICAL CHARACTERISTICS

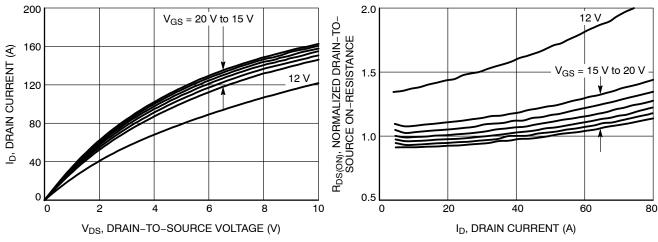


Figure 1. On-Region Characteristics

Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

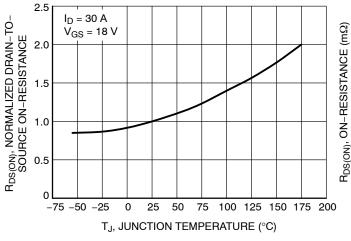


Figure 3. On–Resistance Variation with Temperature

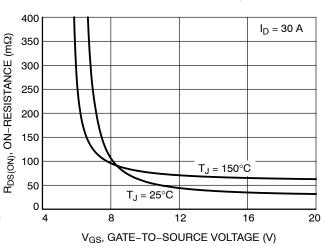


Figure 4. On-Resistance vs. Gate-to-Source Voltage

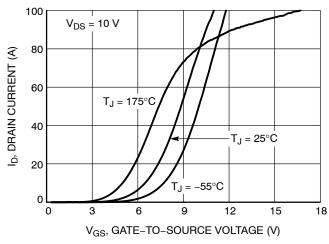


Figure 5. Transfer Characteristics

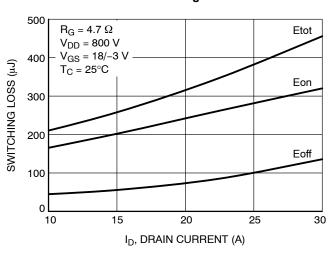


Figure 6. Switching Loss vs. Drain Current

TYPICAL CHARACTERISTICS

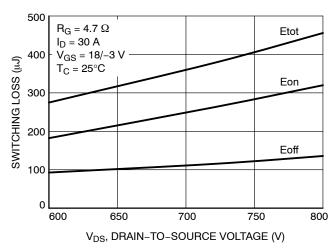


Figure 7. Switching Loss vs. Drain-to-Source Voltage

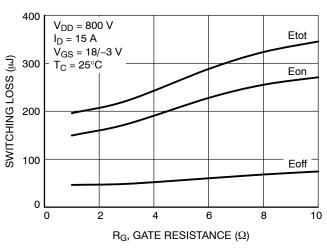


Figure 8. Switching Loss vs. Gate Resistance

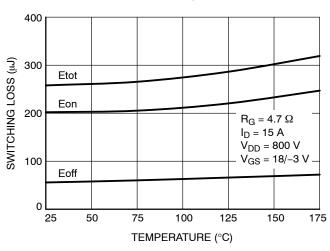


Figure 9. Switching Loss vs. Temperature

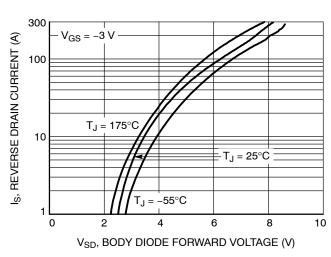


Figure 10. Reverse Drain Current vs. Body Diode Forward Voltage

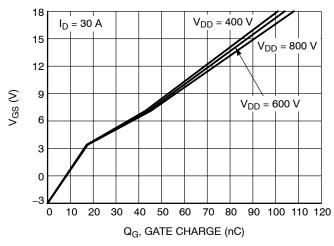


Figure 11. Gate-to-Source Voltage vs. Total Charge

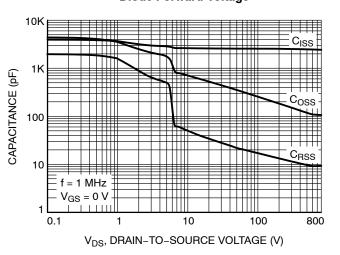


Figure 12. Capacitance vs. Drain-to-Source Voltage

TYPICAL CHARACTERISTICS

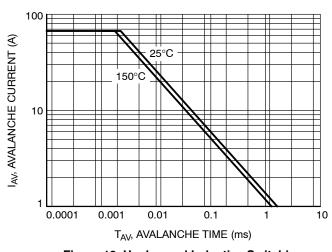


Figure 13. Unclamped Inductive Switching Capability

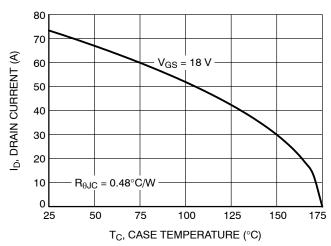


Figure 14. Maximum Continuous Drain Current vs. Case Temperature

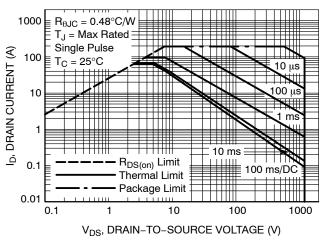


Figure 15. Safe Operating Area

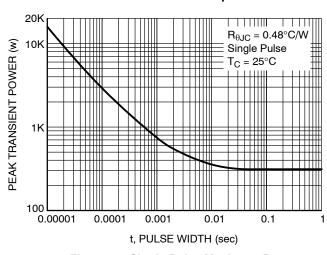


Figure 16. Single Pulse Maximum Power Dissipation

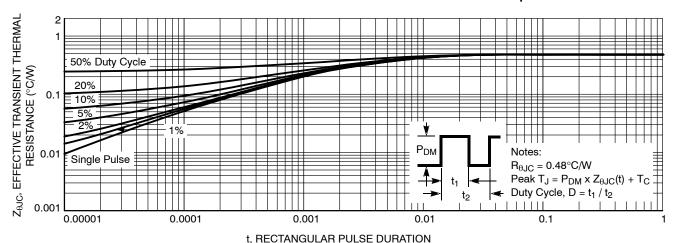


Figure 17. Junction-to-Case Transient Thermal Response

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