# Advance Information N-Channel JFET 15V, 10 to 32mA, 35mS

Automotive JFET designed for compact and efficient designs and including high gain performance. AEC-Q101 qualified JEFT and PPAP capable suitable for automotive applications.

## Features

- Large | yfs |
- Small Ciss
- This small package enables sets to be smaller and thinner
- Ultralow noise figure
- Pb-Free and RoHS compliance
- AEC-Q101 qualified and PPAP capable

## **Typical Applications**

- AM Tuner RF Amplification
- Low Noise Amplifier

## SPECIFICATIONS

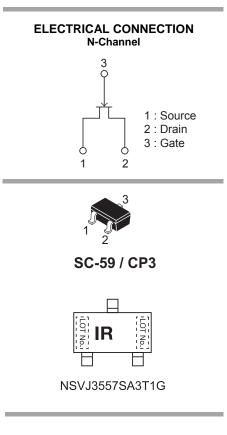
### ABSOLUTE MAXIMUM RATINGS at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSX</sub>	15	V
Gate-to-Drain Voltage	V <sub>GDS</sub>	-15	V
Gate Current	IG	10	mA
Drain Current	۱ <sub>D</sub>	50	mA
Allowable Power Dissipation	PD	200	mW
Operating Junction and Storage Temperature	T <sub>J,</sub> T <sub>Stg</sub>	-55 to+150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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#### ORDERING INFORMATION

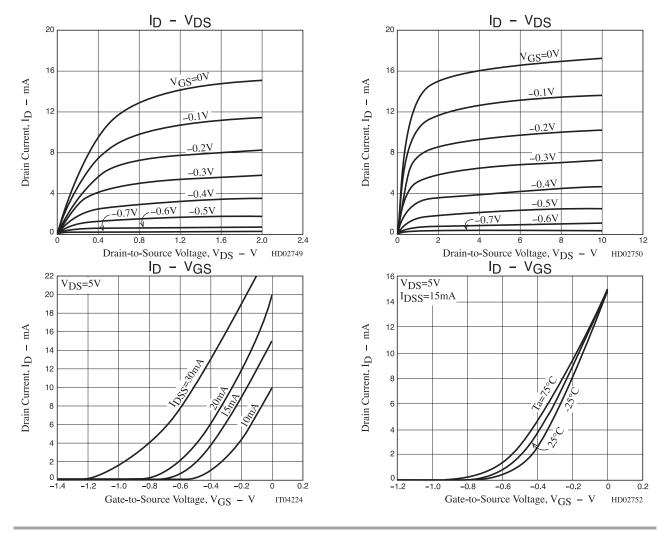
See detailed ordering and shipping information on page 5 of this data sheet

This document contains information on a new product. Specifications and information herein are subject to change without notice.

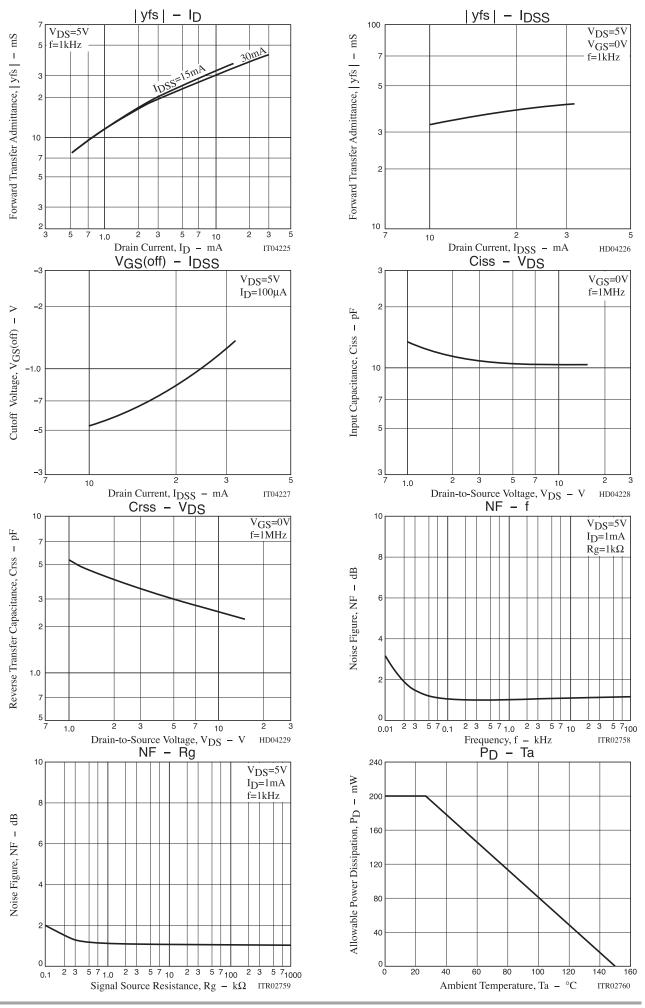
## ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

Deremeter	Symbol	Conditions	Value			Unit
Parameter Symbol Conditions		min	typ	max		
Gate-to-Drain Breakdown Voltage	V <sub>(BR)</sub> G DS	I <sub>G</sub> = -10μΑ, V <sub>DS</sub> = 0V	-15			V
Gate Cutoff Current	IGSS	V <sub>GS</sub> = -10V, V <sub>DS</sub> = 0V			-1	nA
Cutoff Voltage	V <sub>GS(off</sub> )	V <sub>DS</sub> = 5V, I <sub>D</sub> = 100μA	-0.3	-0.7	-1.5	V
Drain Current	IDSS	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 0V	10		32	mA
Forward Transfer Admittance	yfs	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 0V, f = 1kHz	24	35		mS
Input Capacitance	Ciss	VDS = 5V, VGS = 0V, f = 1MHz		10		pF
Reverse Transfer Capacitance	Crss	$v_{DS} = 3v, v_{GS} = 0v, t = 10012$		2.9		pF
Noise Figure	NF	$V_{DS}$ = 5V, Rg=1k $\Omega$ , I <sub>D</sub> =1mA, f=1kHz		1		dB

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



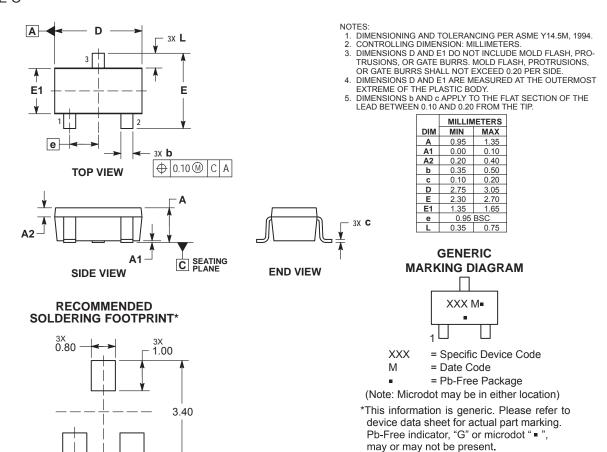
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#### PACKAGE DIMENSIONS unit : mm

SC-59 / CP3 CASE 318BJ ISSUE O



details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering

0.95 PITCH

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#### ORDERING INFORMATION

Device	Marking	Package	Shipping
NSVJ3557SA3T1G	IR	SC-59 3-Lead / CP3 (Pb-Free)	3,000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

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