

# NC7S14

## TinyLogic HS Inverter with Schmitt Trigger Input

### Description

The NC7S14 is a single high performance CMOS Inverter with Schmitt Trigger input. The circuit design provides hysteresis between the positive-going and negative going input thresholds thereby improving noise margins.

Advanced Silicon Gate CMOS fabrication assures high speed and low power circuit operation over a broad  $V_{CC}$  range. ESD protection diodes inherently guard both input and output with respect to the  $V_{CC}$  and GND rails.

### Features

- Space Saving SC-74A and SC-88A 5-Lead Package
- Ultra Small MicroPak™ Leadless Package
- Schmitt Input Hysteresis:  $>1$  V Typ
- High Speed:  $t_{PD} = 4.5$  ns Typ
- Low Quiescent Power:  $I_{CC} < 1$   $\mu$ A
- Balanced Output Drive: 2 mA  $I_{OL}$ , -2 mA  $I_{OH}$
- Broad  $V_{CC}$  Operating Range: 2 V – 6 V
- Balanced Propagation Delays
- Specified for 3 V Operation
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

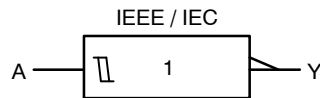


Figure 1. Logic Symbol



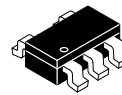
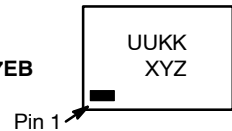
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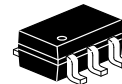
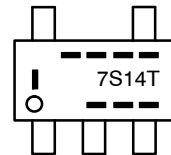
### MARKING DIAGRAMS



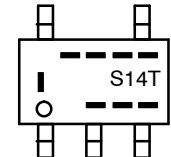
SIP6  
CASE 127EB



SC-74A  
CASE 318BQ



SC-88A  
CASE 419AC-01



UU, 7S14, S14 = Specific Device Code  
 KK = 2-Digit Lot Run Traceability Code  
 XY = 2-Digit Date Code Format  
 Z = Assembly Plant Code  
 T = Die Run Code  
 --- = Year Coding Scheme  
 !-- = Plant Code Identifier  
 --- = Eight-Week Datacoding Scheme

### ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

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## Pin Configurations

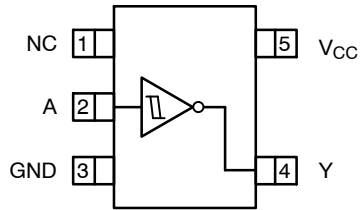


Figure 2. SC-88A and SC-74A (Top View)

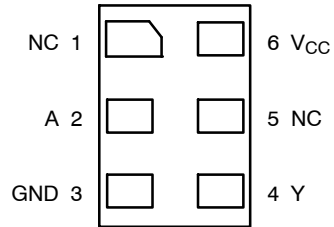


Figure 3. MicroPak (Top Through View)

## PIN DESCRIPTIONS

| Pin Name | Description |
|----------|-------------|
| A        | Input       |
| Y        | Output      |
| NC       | No Connect  |

## FUNCTION TABLE ( $Y = \bar{A}$ )

| Inputs | Output |
|--------|--------|
| A      | Y      |
| L      | H      |
| H      | L      |

H = HIGH Logic Level  
L = LOW Logic Level

## ABSOLUTE MAXIMUM RATINGS

| Symbol                | Parameter                                    | Min                                 | Max            | Unit               |    |
|-----------------------|--|-------------------------------------|----------------|--------------------|----|
| $V_{CC}$              | Supply Voltage                               | -0.5                                | 6.5            | V                  |    |
| $I_{IK}$              | DC Input Diode Current                       | $V_{IN} \leq -0.5\text{ V}$         | -              | -20                | mA |
|                       |  | $V_{IN} \geq V_{CC} + 0.5\text{ V}$ | -              | +20                |    |
| $V_{IN}$              | DC Input Voltage                             | -0.5                                | $V_{CC} + 0.5$ | V                  |    |
| $I_{OK}$              | DC Output Diode Current                      | $V_{OUT} < -0.5\text{ V}$           | -              | -20                | mA |
|                       |  | $V_{OUT} > V_{CC} + 0.5\text{ V}$   | -              | +20                |    |
| $V_{OUT}$             | DC Output Voltage                            | -0.5                                | $V_{CC} + 0.5$ | V                  |    |
| $I_{OUT}$             | DC Output Source or Sink Current             | -                                   | $\pm 12.5$     | mA                 |    |
| $I_{CC}$ or $I_{GND}$ | DC $V_{CC}$ or Ground Current per Output Pin | -                                   | $\pm 25$       | mA                 |    |
| $T_{STG}$             | Storage Temperature                          | -65                                 | +150           | $^{\circ}\text{C}$ |    |
| $T_J$                 | Junction Temperature                         | -                                   | +150           | $^{\circ}\text{C}$ |    |
| $T_L$                 | Lead Temperature (Soldering, 10 Seconds)     | -                                   | +260           | $^{\circ}\text{C}$ |    |
| $P_D$                 | Power Dissipation in Still Air               | SC-74A                              | -              | 225                | mW |
|                       |  | SC-88A-5                            | -              | 190                |    |
|                       |  | MicroPak                            | -              | 327                |    |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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## RECOMMENDED OPERATING CONDITIONS

| Symbol           | Parameter             | Conditions | Min | Max             | Unit |
|------------------|-----------------------|------------|-----|-----------------|------|
| V <sub>CC</sub>  | Supply Voltage        |            | 2.0 | 6.0             | V    |
| V <sub>IN</sub>  | Input Voltage         |            | 0   | V <sub>CC</sub> | V    |
| V <sub>OUT</sub> | Output Voltage        |            | 0   | V <sub>CC</sub> | V    |
| T <sub>A</sub>   | Operating Temperature |            | -40 | +85             | °C   |
| θ <sub>JA</sub>  | Thermal Resistance    | SC-74A     | -   | 555             | °C/W |
|                  |                       | SC-88A-5   | -   | 659             |      |
|                  |                       | MicroPak   | -   | 382             |      |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

1. Unused inputs must be held HIGH or LOW. They may not float.

## DC ELECTRICAL CHARACTERISTICS

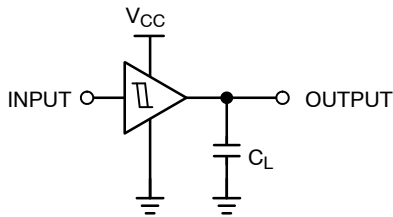
| Symbol          | Parameter                  | V <sub>CC</sub> (V) | Conditions  | T <sub>A</sub> = +25°C   |      |      | T <sub>A</sub> = -40 to +85°C |      | Unit |   |
|-----------------|----------------------------|---------------------|---|--|------|------|-------------------------------|------|------|---|
|                 |                            |                     |   | Min  | Typ  | Max  | Min                           | Max  |      |   |
| V <sub>P</sub>  | Positive Threshold Voltage | 2.0                 |   | -  | 1.29 | 1.5  | -                             | 1.6  | V    |   |
|                 |                            | 3.0                 |   | -  | 1.90 | 2.2  | -                             | 2.2  |      |   |
|                 |                            | 4.5                 |   | -  | 2.73 | 3.15 | -                             | 3.15 |      |   |
|                 |                            | 6.0                 |   | -  | 3.56 | 4.2  | -                             | 4.2  |      |   |
| V <sub>N</sub>  | Negative Threshold Voltage | 2.0                 |   | 0.3  | 0.70 | -    | 0.3                           | -    | V    |   |
|                 |                            | 3.0                 |   | 0.6  | 1.05 | -    | 0.6                           | -    |      |   |
|                 |                            | 4.5                 |   | 1.13   | 1.66 | -    | 1.13                          | -    |      |   |
|                 |                            | 6.0                 |   | 1.5  | 2.24 | -    | 1.5                           | -    |      |   |
| V <sub>H</sub>  | Hysteresis Voltage         | 2.0                 |   | 0.3  | 0.59 | 1.0  | 0.3                           | 1.0  | V    |   |
|                 |                            | 3.0                 |   | 0.4  | 0.85 | 1.3  | 0.4                           | 1.3  |      |   |
|                 |                            | 4.5                 |   | 0.6  | 1.08 | 1.4  | 0.6                           | 1.4  |      |   |
|                 |                            | 6.0                 |   | 0.8  | 1.31 | 1.7  | 0.8                           | 1.7  |      |   |
| V <sub>OH</sub> | HIGH Level Output Voltage  | 2.0                 | I <sub>OH</sub> = -20 μA<br>V <sub>IN</sub> = V <sub>IL</sub> | 1.90   | 2.0  | -    | 1.90                          | -    | V    |   |
|                 |                            | 3.0                 |   | 2.90   | 3.0  | -    | 2.90                          | -    |      |   |
|                 |                            | 4.5                 |   | 4.40   | 4.5  | -    | 4.40                          | -    |      |   |
|                 |                            | 6.0                 |   | 5.90   | 6.0  | -    | 5.90                          | -    |      |   |
|                 |                            |                     | 3.0   | V <sub>IN</sub> = V <sub>IL</sub><br>I <sub>OH</sub> = -1.3 mA<br>I <sub>OH</sub> = -2.0 mA<br>I <sub>OH</sub> = -2.6 mA | 2.68 | 2.87 | -                             | 2.63 | -    | V |
|                 |                            |                     | 4.5   |  | 4.18 | 4.37 | -                             | 4.13 | -    |   |
|                 |                            |                     | 6.0   |  | 5.68 | 5.86 | -                             | 5.63 | -    |   |
|                 |                            |                     |   |  |      |      |                               |      |      |   |
| V <sub>OL</sub> | LOW Level Output Voltage   | 2.0                 | I <sub>OL</sub> = 20 μA<br>V <sub>IN</sub> = V <sub>IH</sub>  | -  | 0.0  | 0.10 | -                             | 0.10 | V    |   |
|                 |                            | 3.0                 |   | -  | 0.0  | 0.10 | -                             | 0.10 |      |   |
|                 |                            | 4.5                 |   | -  | 0.0  | 0.10 | -                             | 0.10 |      |   |
|                 |                            | 6.0                 |   | -  | 0.0  | 0.10 | -                             | 0.10 |      |   |
|                 |                            |                     | 3.0   | V <sub>IN</sub> = V <sub>IH</sub><br>I <sub>OL</sub> = 1.3 mA<br>I <sub>OL</sub> = 2.0 mA<br>I <sub>OL</sub> = 2.6 mA    | -    | 0.1  | 0.26                          | -    | 0.33 | V |
|                 |                            |                     | 4.5   |  | -    | 0.1  | 0.26                          | -    | 0.33 |   |
|                 |                            |                     | 6.0   |  | -    | 0.1  | 0.26                          | -    | 0.33 |   |
|                 |                            |                     |   |  |      |      |                               |      |      |   |
| I <sub>IN</sub> | Input Leakage Current      | 6.0                 | V <sub>IN</sub> = V <sub>CC</sub> , GND                       | -  | -    | ±0.1 | -                             | ±1.0 | μA   |   |
| I <sub>CC</sub> | Quiescent Supply Current   | 6.0                 | V <sub>IN</sub> = V <sub>CC</sub> , GND                       | -  | -    | 1.0  | -                             | 10.0 | μA   |   |

AC ELECTRICAL CHARACTERISTICS

| Symbol                               | Parameter                                | V <sub>CC</sub> (V) | Conditions             | T <sub>A</sub> = +25°C |     |     | T <sub>A</sub> = -40 to +85°C |     | Unit |
|--------------------------------------|--|---------------------|------------------------|------------------------|-----|-----|-------------------------------|-----|------|
|                                      |  |                     |                        | Min                    | Typ | Max | Min                           | Max |      |
| t <sub>PLH</sub><br>t <sub>PHL</sub> | Propagation Delay (Figure 4, 6)          | 5.0                 | C <sub>L</sub> = 15 pF | -                      | 4.5 | 21  | -                             | -   | ns   |
|                                      |  | 2.0                 | C <sub>L</sub> = 50 pF | -                      | 20  | 100 | -                             | 125 | ns   |
|                                      |  | 3.0                 |                        | -                      | 12  | 27  | -                             | 35  |      |
|                                      |  | 4.5                 |                        | -                      | 8.5 | 20  | -                             | 25  |      |
| 6.0                                  | -  | 7.5                 | 17                     | -                      | 21  |     |                               |     |      |
| t <sub>TLH</sub><br>t <sub>THL</sub> | Output Transition Time (Figure 4, 6)     | 5.0                 | C <sub>L</sub> = 15 pF | -                      | 3   | 8   | -                             | -   | ns   |
|                                      |  | 2.0                 | C <sub>L</sub> = 50 pF | -                      | 25  | 125 | -                             | 145 | ns   |
|                                      |  | 3.0                 |                        | -                      | 16  | 35  | -                             | 45  |      |
|                                      |  | 4.5                 |                        | -                      | 11  | 25  | -                             | 30  |      |
| 6.0                                  | -  | 9                   | 21                     | -                      | 24  |     |                               |     |      |
| C <sub>IN</sub>                      | Input Capacitance                        | Open                |                        | -                      | 2   | 10  | -                             | 10  | pF   |
| C <sub>PD</sub>                      | Power Dissipation Capacitance (Figure 5) | 5.0                 | (Note 2)               | -                      | 7   | -   | -                             | -   | pF   |

2. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I<sub>CCD</sub>) at no output loading and operating at 50% duty cycle. C<sub>PD</sub> is related to I<sub>CCD</sub> dynamic operating current by the expression:  
 $I_{CCD} = (C_{PD})(V_{CC})(f_{IN}) + (I_{CCstatic})$ .

AC Loading and Waveforms



C<sub>L</sub> includes load and stray capacitance  
 Input PRR = 1.0 MHz; t<sub>W</sub> = 500 ns

Figure 4. AC Test Circuit

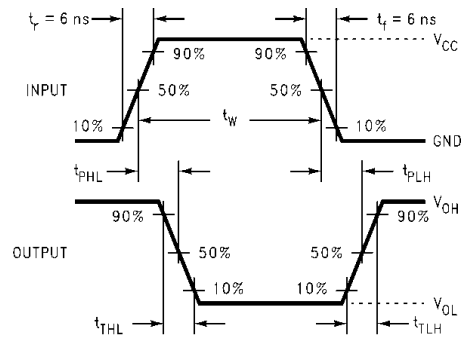
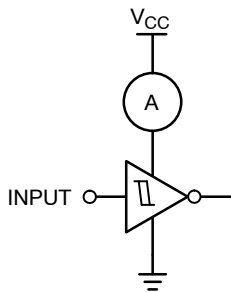


Figure 6. AC Waveforms



Input = AC Waveforms;  
 PRR = Variable; Duty Cycle = 50%.

Figure 5. I<sub>CCD</sub> Test Circuit

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## ORDERING INFORMATION

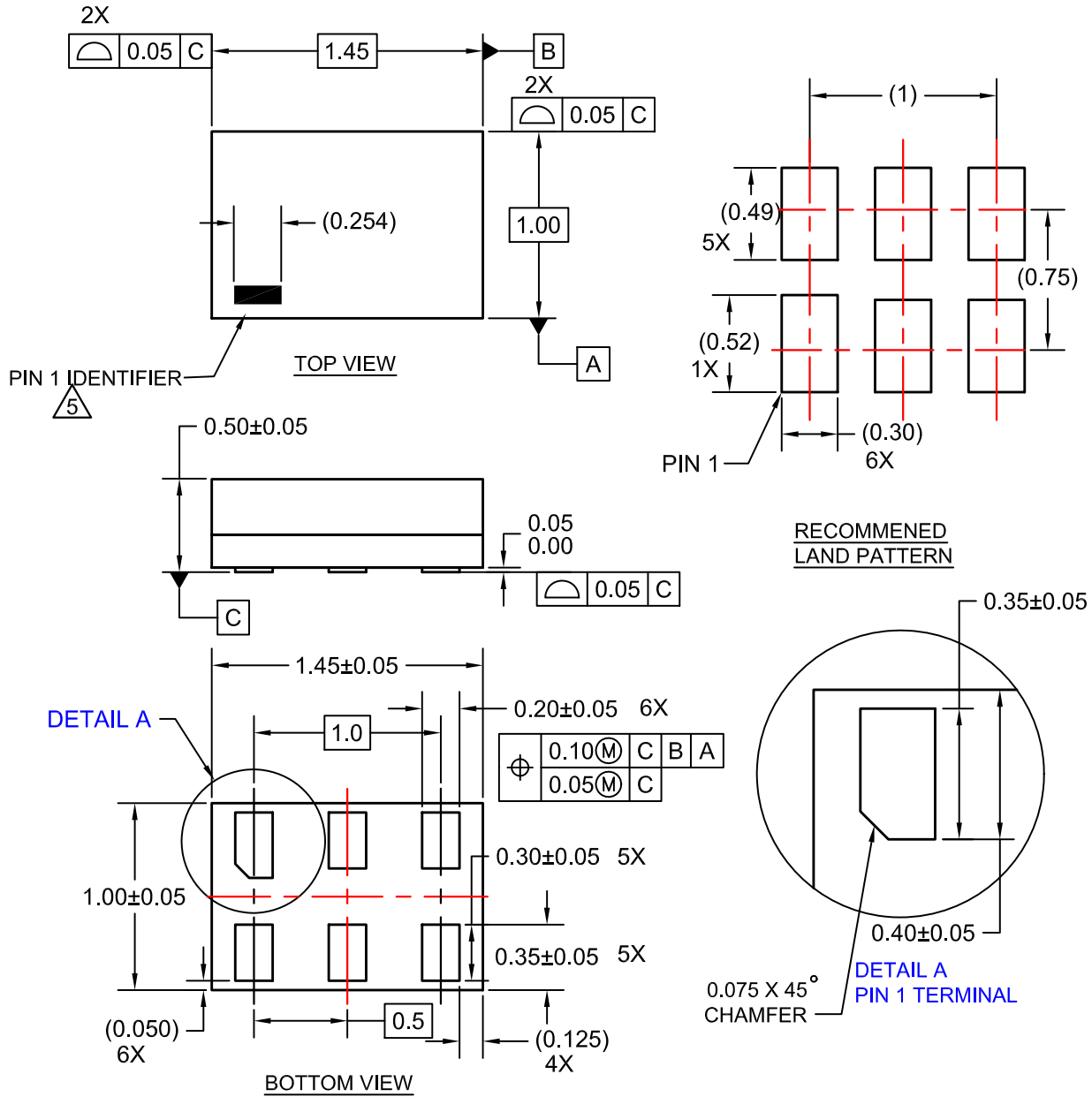
| Part Number | Top Mark | Package Description | Shipping†          |
|-------------|----------|---------------------|--------------------|
| NC7S14M5X   | 7S14     | SC-74A              | 3000 / Tape & Reel |
| NC7S14P5X   | S14      | SC-88A              | 3000 / Tape & Reel |
| NC7S14L6X   | UU       | SIP6, MicroPak      | 5000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NC7S14

## PACKAGE DIMENSIONS

SIP6 1.45X1.0  
CASE 127EB  
ISSUE O



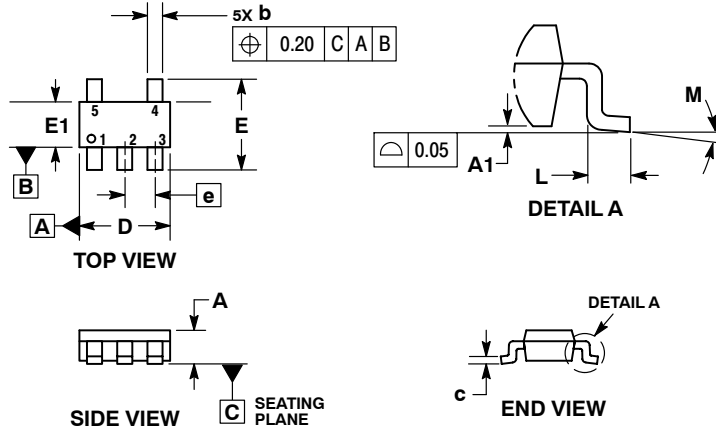
**NOTES:**

1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-2009
4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

# NC7S14

## PACKAGE DIMENSIONS

### SC-74A CASE 318BQ ISSUE B

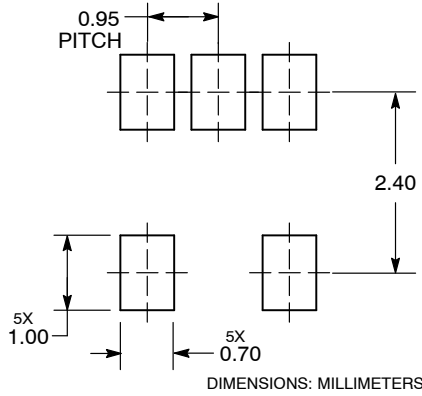


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE.

| DIM | MILLIMETERS |      |
|-----|-------------|------|
|     | MIN         | MAX  |
| A   | 0.90        | 1.10 |
| A1  | 0.01        | 0.10 |
| b   | 0.25        | 0.50 |
| c   | 0.10        | 0.26 |
| D   | 2.85        | 3.15 |
| E   | 2.50        | 3.00 |
| E1  | 1.35        | 1.65 |
| e   | 0.95 BSC    |      |
| L   | 0.20        | 0.60 |
| M   | 0°          | 10°  |

### RECOMMENDED SOLDERING FOOTPRINT\*

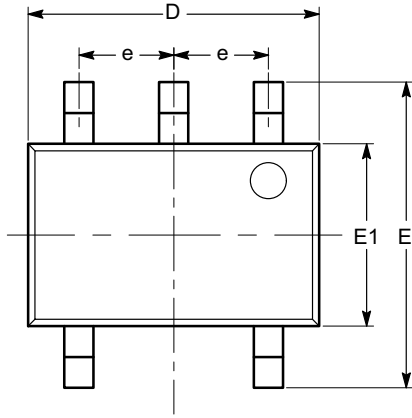


\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# NC7S14

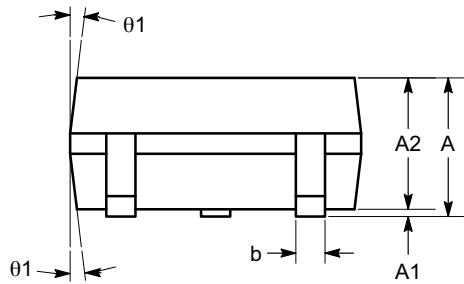
## PACKAGE DIMENSIONS

SC-88A (SC-70 5 Lead), 1.25x2  
CASE 419AC-01  
ISSUE A

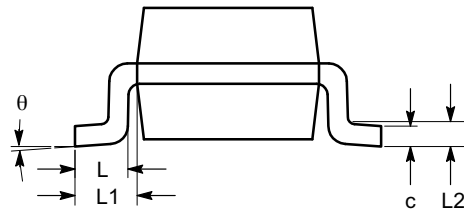


TOP VIEW

| SYMBOL     | MIN      | NOM  | MAX  |
|------------|----------|------|------|
| A          | 0.80     |      | 1.10 |
| A1         | 0.00     |      | 0.10 |
| A2         | 0.80     |      | 1.00 |
| b          | 0.15     |      | 0.30 |
| c          | 0.10     |      | 0.18 |
| D          | 1.80     | 2.00 | 2.20 |
| E          | 1.80     | 2.10 | 2.40 |
| E1         | 1.15     | 1.25 | 1.35 |
| e          | 0.65 BSC |      |      |
| L          | 0.26     | 0.36 | 0.46 |
| L1         | 0.42 REF |      |      |
| L2         | 0.15 BSC |      |      |
| $\theta$   | 0°       |      | 8°   |
| $\theta 1$ | 4°       |      | 10°  |



SIDE VIEW




END VIEW

**Notes:**

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-203.



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