## 6-Pin DIP High Voltage Photodarlington Optocouplers

# H11G1M, H11G2M

## Description

The H11G1M and H11G2M are photodarlington-type optically coupled optocouplers. These devices have a gallium arsenide infrared emitting diode coupled with a silicon darlington connected phototransistor which has an integral base-emitter resistor to optimize elevated temperature characteristics.

## Features

- High BV<sub>CEO</sub>:
  - 100 V Minimum for H11G1M
  - 80 V Minimum for H11G2M
- High Sensitivity to Low Input Current (Minimum 500% CTR at I<sub>F</sub> = 1 mA)
- Low Leakage Current at Elevated Temperature (Maximum 100 μA at 80°C)
- Safety and Regulatory Approvals:
  - UL1577, 4,170 VAC<sub>RMS</sub> for 1 Minute
  - DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

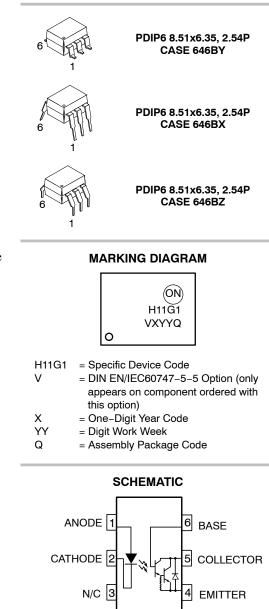
## Application

- CMOS Logic Interface
- Telephone Ring Detector
- Low Input TTL Interface
- Power Supply Isolation
- Replace Pulse Transformer



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## **ORDERING INFORMATION**

See detailed ordering and shipping information on page 6 of this data sheet.

## SAFETY AND INSULATION RATINGS

(As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.)

Parameter		Characteristics
Installation Classifications per DIN VDE 0110/1.89 Table 1,	<150 V <sub>RMS</sub>	I–IV
For Rated Mains Voltage	<300 V <sub>RMS</sub>	I–IV
Climatic Classification	55/100/21	
Pollution Degree (DIN VDE 0110/1.89)	Pollution Degree (DIN VDE 0110/1.89)	
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
$V_{PR}$	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with t <sub>m</sub> = 10 s, Partial Discharge < 5 pC	1360	V <sub>peak</sub>
	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$ , 100% Production Test with $t_m = 1 s$ , Partial Discharge < 5 pC	1594	V <sub>peak</sub>
VIORM	Maximum Working Insulation Voltage	850	V <sub>peak</sub>
V <sub>IOTM</sub>	Highest Allowable Over-Voltage	6000	V <sub>peak</sub>
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥0.5	mm
TS	Case Temperature (Note 1)	175	°C
I <sub>S,INPUT</sub>	Input Current (Note 1)	350	mA
P <sub>S,OUTPUT</sub>	Output Power (Note 1)	800	mW
R <sub>IO</sub>	Insulation Resistance at T <sub>S</sub> , V <sub>IO</sub> = 500 V (Note 1)	>10 <sup>9</sup>	Ω

1. Safety limit values - maximum values allowed in the event of a failure.

## **ABSOLUTE MAXIMUM RATINGS**

Derate Above 25°C

Symbol	Parameter	Parameter		
OTAL DEV	ICE			
T <sub>STG</sub>	Storage Temperature		-40 to +125	°C
T <sub>OPR</sub>	Operating Temperature		-40 to +100	°C
TJ	Junction Temperature		-40 to +125	°C
T <sub>SOL</sub>	Lead Solder Temperature		260 for 10 seconds	°C
PD	Total Device Power Dissipation @ T <sub>A</sub> = 25°C		290	mW
	Derate Above 25°C	3.5	mW/°C	
MITTER				
١ <sub>F</sub>	DC / Average Forward Input Current		60	mA
V <sub>R</sub>	Reverse Input Voltage		6.0	V
l <sub>F</sub> (pk)	Forward Current – Peak (1 μs pulse, 300 pps)		3.0	А
PD	LED Power Dissipation @ T <sub>A</sub> = 25°C		90	mW
	Derate Above 25°C		1.8	mW/°C
ETECTOR				
V <sub>CEO</sub>	Collector Emitter Voltage	H11G1M	100	V
		H11G2M	80	V
PD	Photodetector Power Dissipation @ T <sub>A</sub> = 25°C	•	200	mW

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2.67

mW/°C

## **ELECTRICAL CHARACTERISTICS - INDIVIDUAL COMPONENT CHARACTERISTICS**

(T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter		Test Conditions	Min	Тур	Max	Unit
EMITTER							
VF	Forward Voltage		I <sub>F</sub> = 10 mA	-	1.3	1.5	V
$\Delta V_{F} / \Delta T_{A}$	Forward Voltage Temperature C	oefficient		-	-1.8	-	mV/°C
BV <sub>R</sub>	Reverse Breakdown Voltage		I <sub>R</sub> = 10 μA	3.0	25	-	V
CJ	Junction Capacitance	Junction Capacitance		-	50	-	pF
			V <sub>F</sub> = 1 V, f = 1 MHz	-	65	-	pF
I <sub>R</sub>	Reverse Leakage Current	Reverse Leakage Current		-	0.001	10	μA
DETECTOR							
BV <sub>CEO</sub>	Breakdown Voltage Collector	H11G1M	I <sub>C</sub> = 1.0 mA, I <sub>F</sub> = 0	100	-	-	V
	to Emitter	H11G2M		80	-	-	V
BV <sub>CBO</sub>	Collector to Base	H11G1M	I <sub>C</sub> = 100 μA	100	-	-	V
		H11G2M		80	-	-	V
$BV_{EBO}$	Emitter Base	<u>.</u>		7	10	-	V
I <sub>CEO</sub>	Leakage Current Collector	H11G1M	V <sub>CE</sub> = 80 V, I <sub>F</sub> = 0	-	-	100	nA
	to Emitter	1			i		<del>.</del>

BV <sub>CEO</sub>	Breakdown Voltage Collector	H11G1M	I <sub>C</sub> = 1.0 mA, I <sub>F</sub> = 0	100	-	-	V
	to Emitter	H11G2M		80	-	-	V
BV <sub>CBO</sub>	Collector to Base	H11G1M	I <sub>C</sub> = 100 μA	100	-	-	V
		H11G2M		80	-	-	V
$BV_{EBO}$	Emitter Base			7	10	-	V
I <sub>CEO</sub>	Leakage Current Collector	H11G1M	$V_{CE} = 80 \text{ V}, I_F = 0$	-	-	100	nA
	to Emitter	H11G2M	$V_{CE} = 60 \text{ V}, I_F = 0$	-	-	100	nA
		H11G1M	$V_{CE} = 80 \text{ V}, \text{ I}_{F} = 0, \text{ T}_{A} = 80^{\circ}\text{C}$	-	-	100	μΑ
		H11G2M	$V_{CE} = 60 \text{ V}, I_F = 0, T_A = 80^{\circ}\text{C}$	-	-	100	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## ELECTRICAL CHARACTERISTICS – TRANSFER CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
EMITTER						
CTR	Current Transfer Ratio, Collector to Emitter	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 1 V	100 (1000)	-	-	mA (%)
		I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V	5 (500)	-	-	mA (%)
V <sub>CE (SAT)</sub>	Saturation Voltage	I <sub>F</sub> = 16 mA, I <sub>C</sub> = 50 mA,	-	0.85	1.0	V
		I <sub>F</sub> = 1 mA, I <sub>C</sub> = 1 mA,	-	0.75	1.0	V

#### SWITCHING TIMES

	t <sub>ON</sub>	Turn on Time	$R_L$ = 100 Ω, $I_F$ = 10 mA, $V_{CE}$ = 5 V, f ≤ 30 Hz. Pulse Width ≤ 300 us	-	5	-	μs
ſ	t <sub>OFF</sub>	Turn off Time	· _ · · · · _ , · · · · · · · · · · _ · · · ·	-	100	-	μs

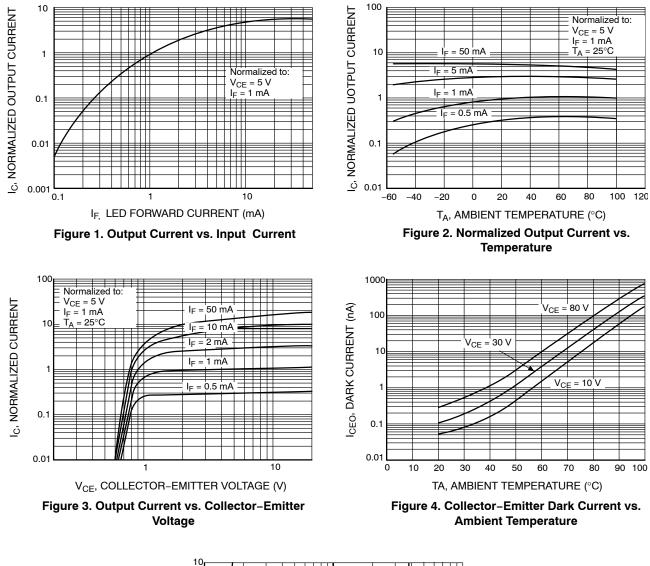
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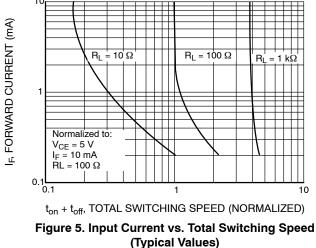
## **ELECTRICAL CHARACTERISTICS** – **ISOLATION CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V <sub>ISO</sub>	Input-Output Isolation Voltage	t = 1 Minute	4170	-	-	VAC <sub>RMS</sub>
C <sub>ISO</sub>	Isolation Capacitance	V <sub>I-O</sub> = 0 V, f = 1 MHz	-	0.2	-	pF
R <sub>ISO</sub>	Isolation Resistance	$V_{I-O}$ = ±500 VDC, $T_A$ = 25°C	10 <sup>11</sup>	-	-	Ω

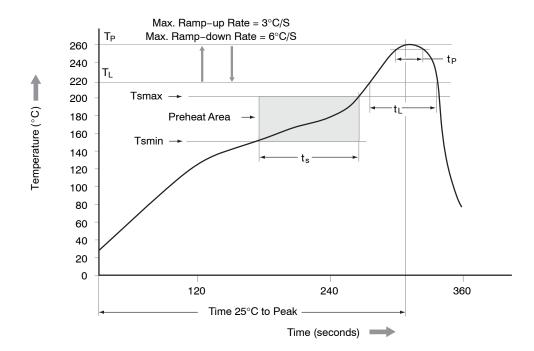
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## **TYPICAL PERFORMANCE CURVES**





## **REFLOW PROFILE**



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60–120 seconds
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t $_{L}$ ) Maintained Above (T $_{L}$ )	60–150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Figure 6. Reflow Profile

## **ORDERING INFORMATION**

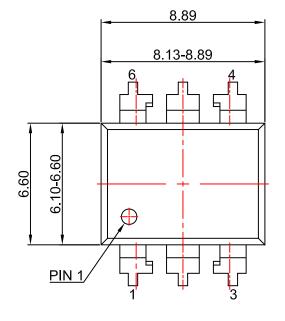
Part Number	Package	<b>Shipping</b> <sup>†</sup>
H11G1M	DIP 6-Pin	50 Units / Tube
H11G1SM	SMT 6-Pin (Lead Bend)	50 Units / Tube
H11G1SR2M	SMT 6-Pin (Lead Bend)	1000 / Tape & Reel
H11G1VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	50 Units / Tube
H11G1SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	50 Units / Tube
H11G1SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	1000 / Tape & Reel
H11G1TVM	DIP 6–Pin, 0.4" Lead Spacing, DIN EN/IEC60747–5–5 Option	50 Units / Tube

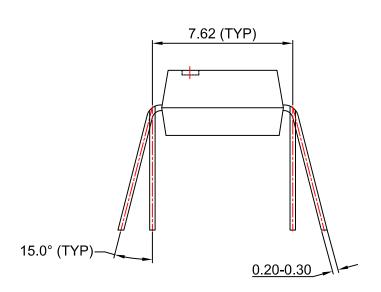
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

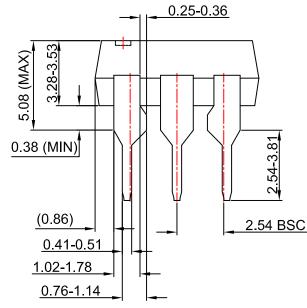


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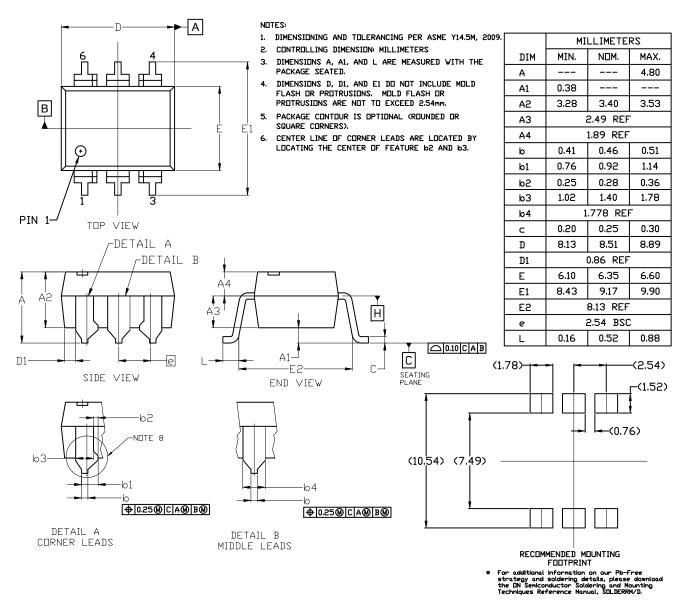
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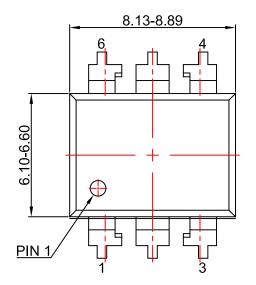


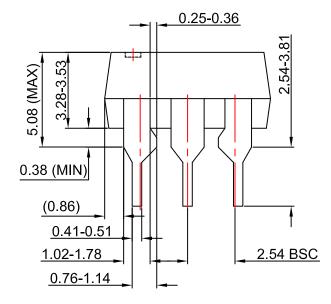
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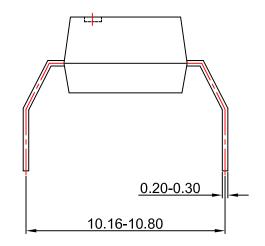


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